

Prior Art

Fig. 1

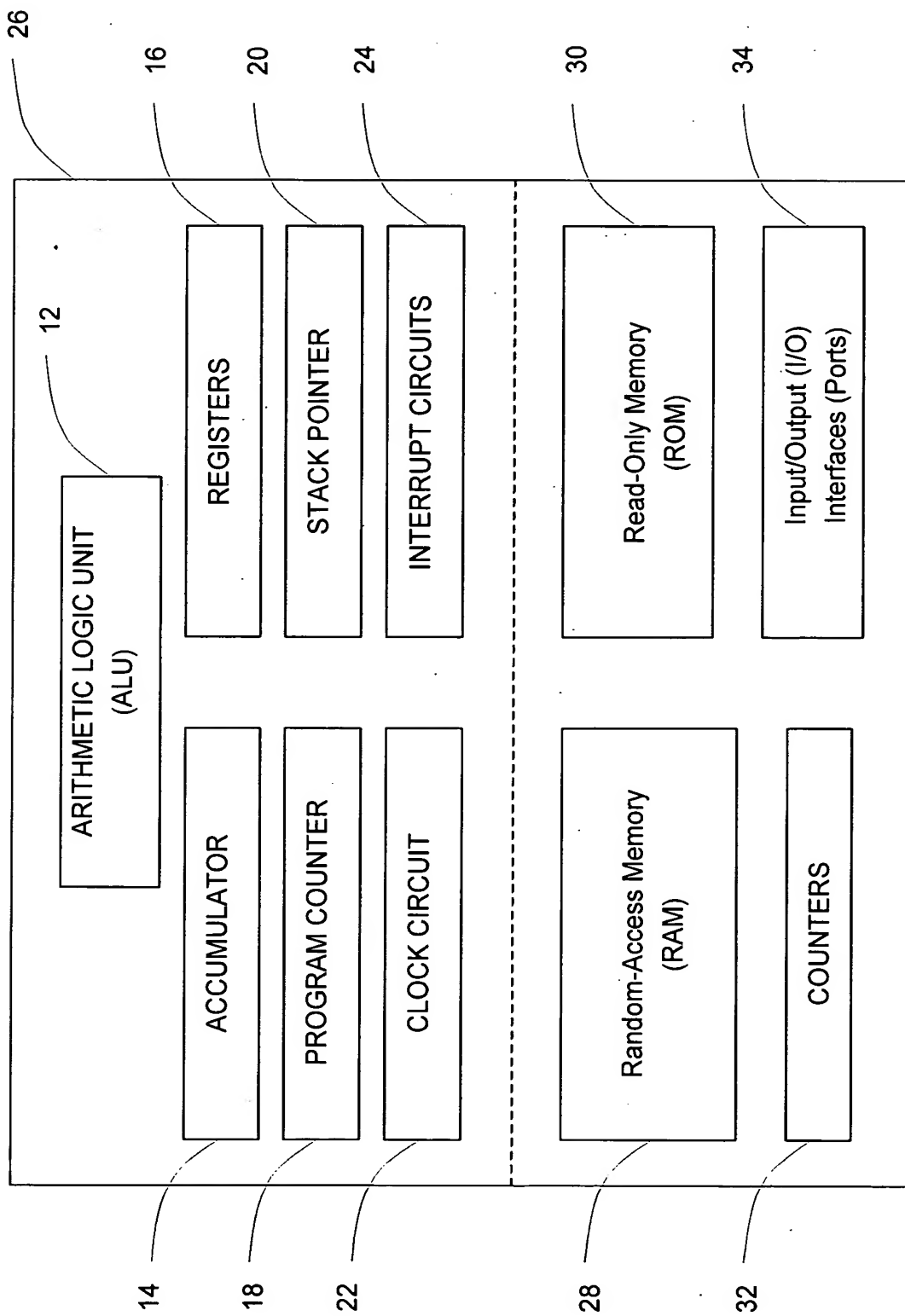


Fig. 2

Prior Art

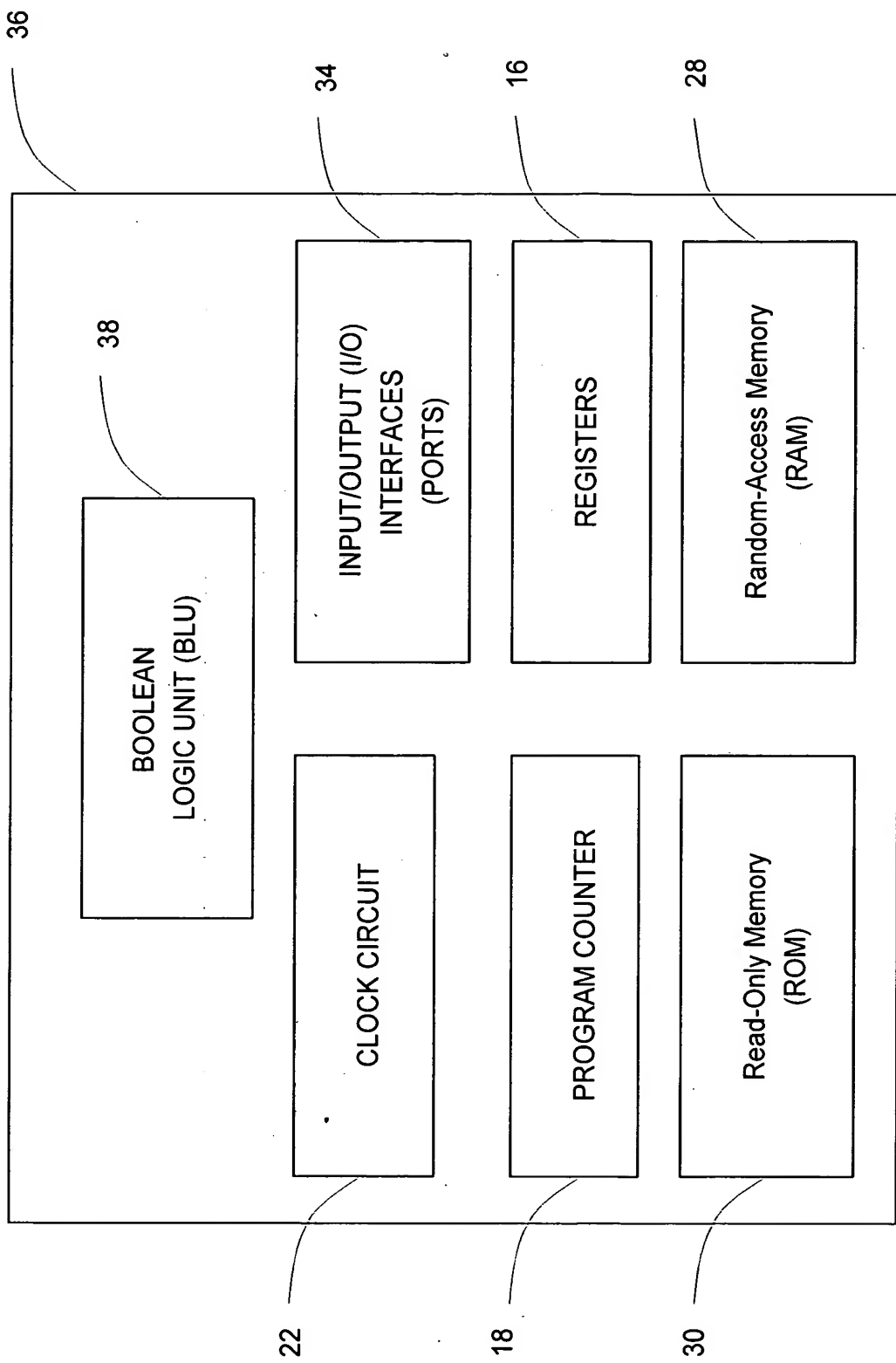


Fig. 3

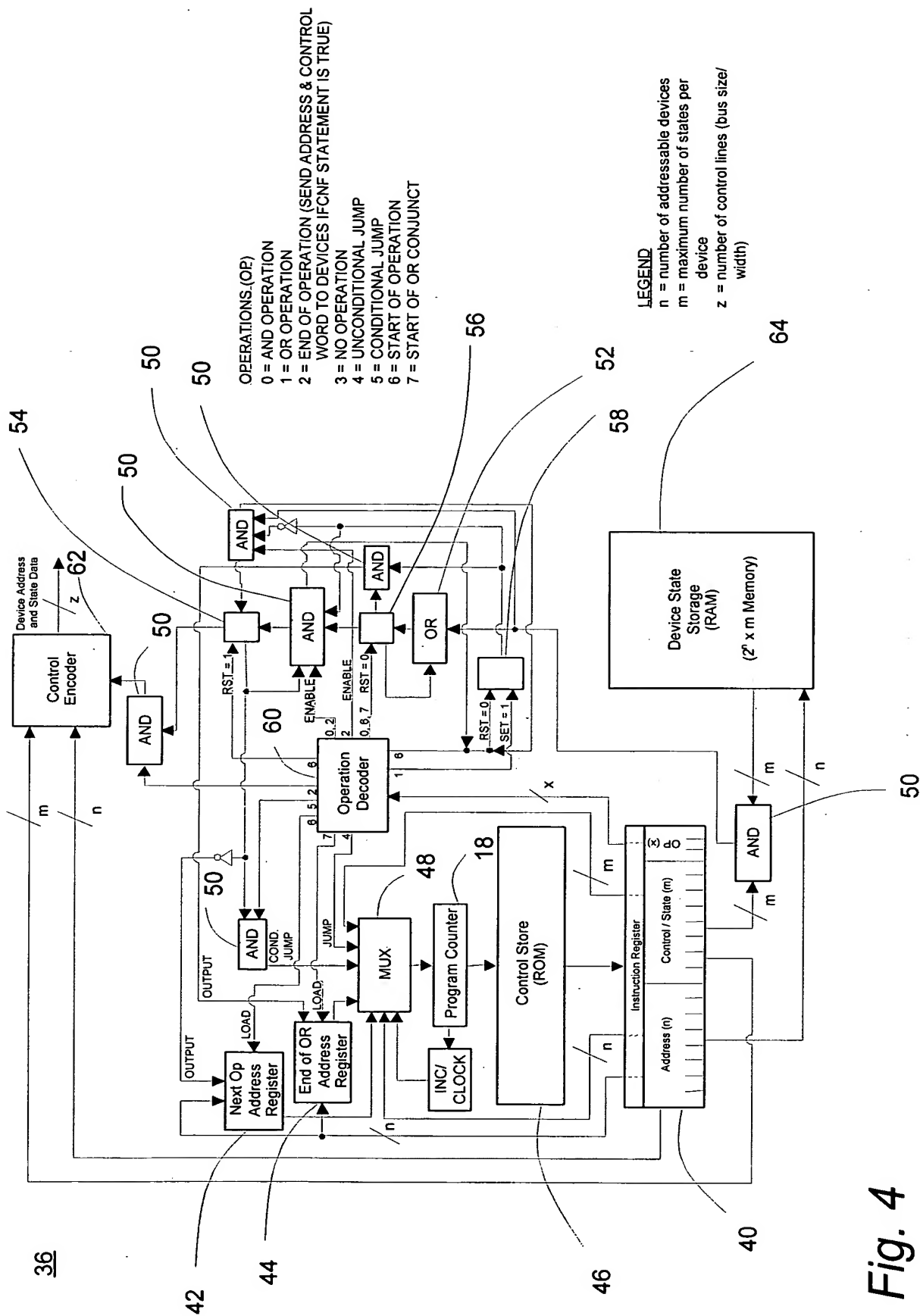


Fig. 5

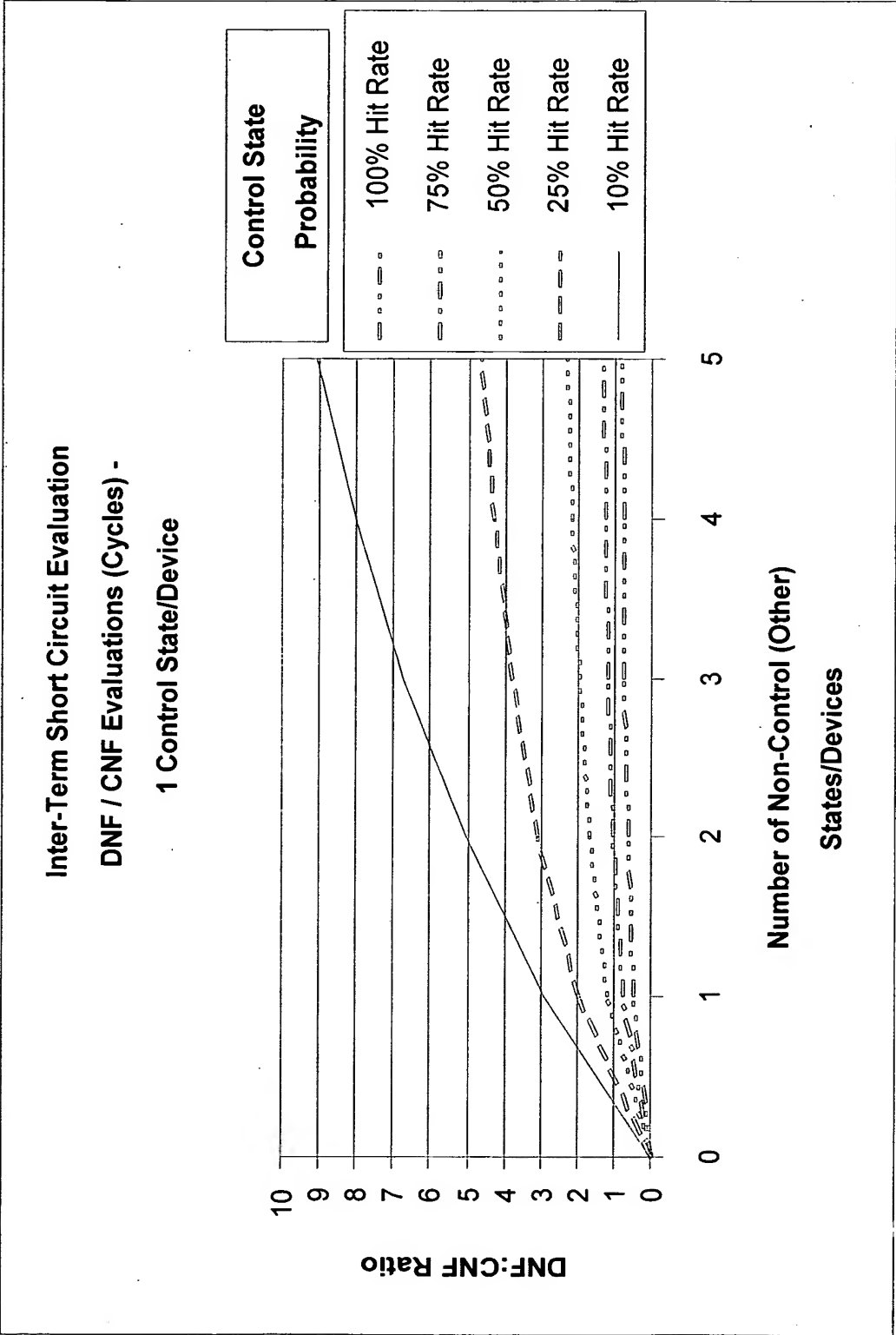


Fig. 6

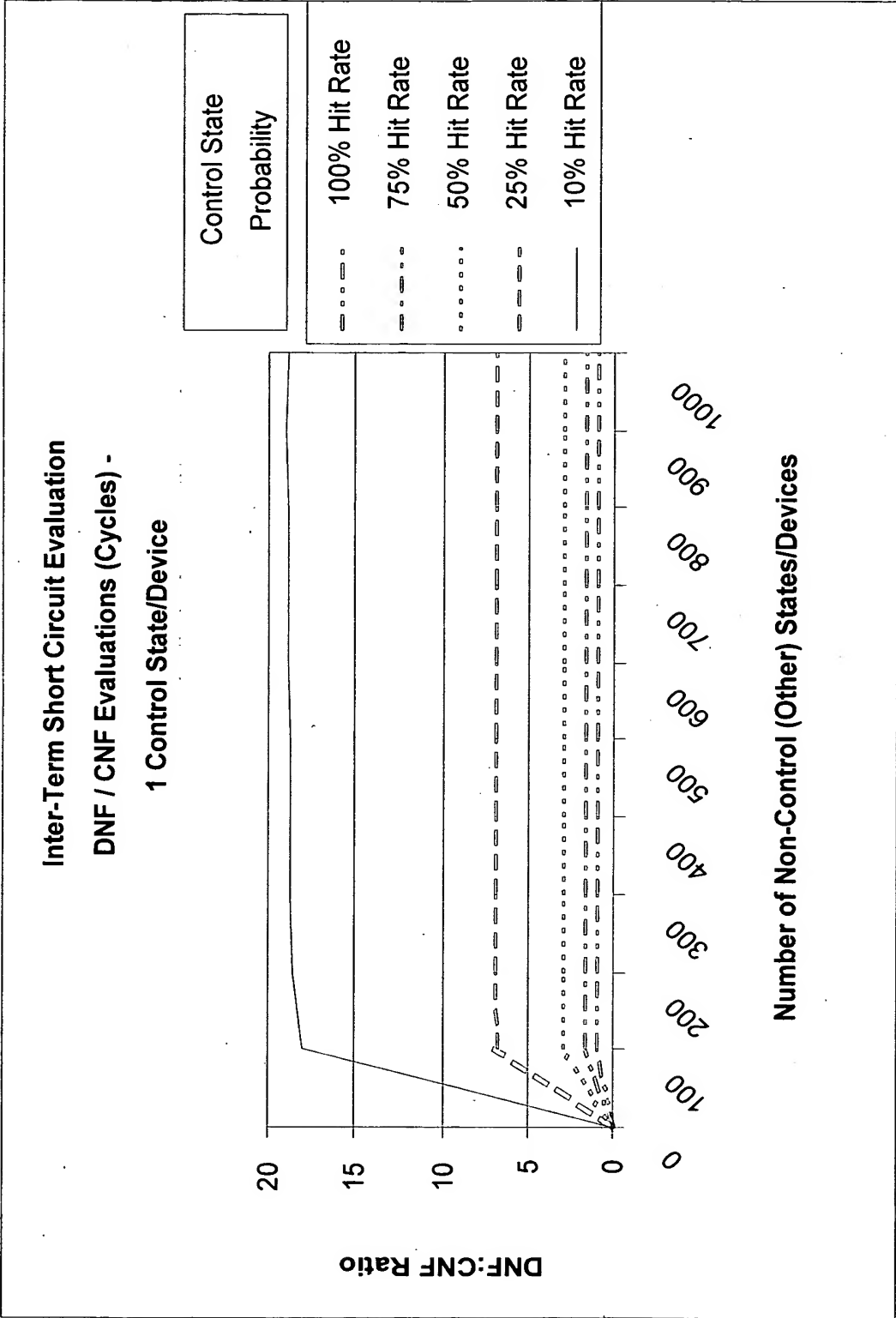


Fig. 7

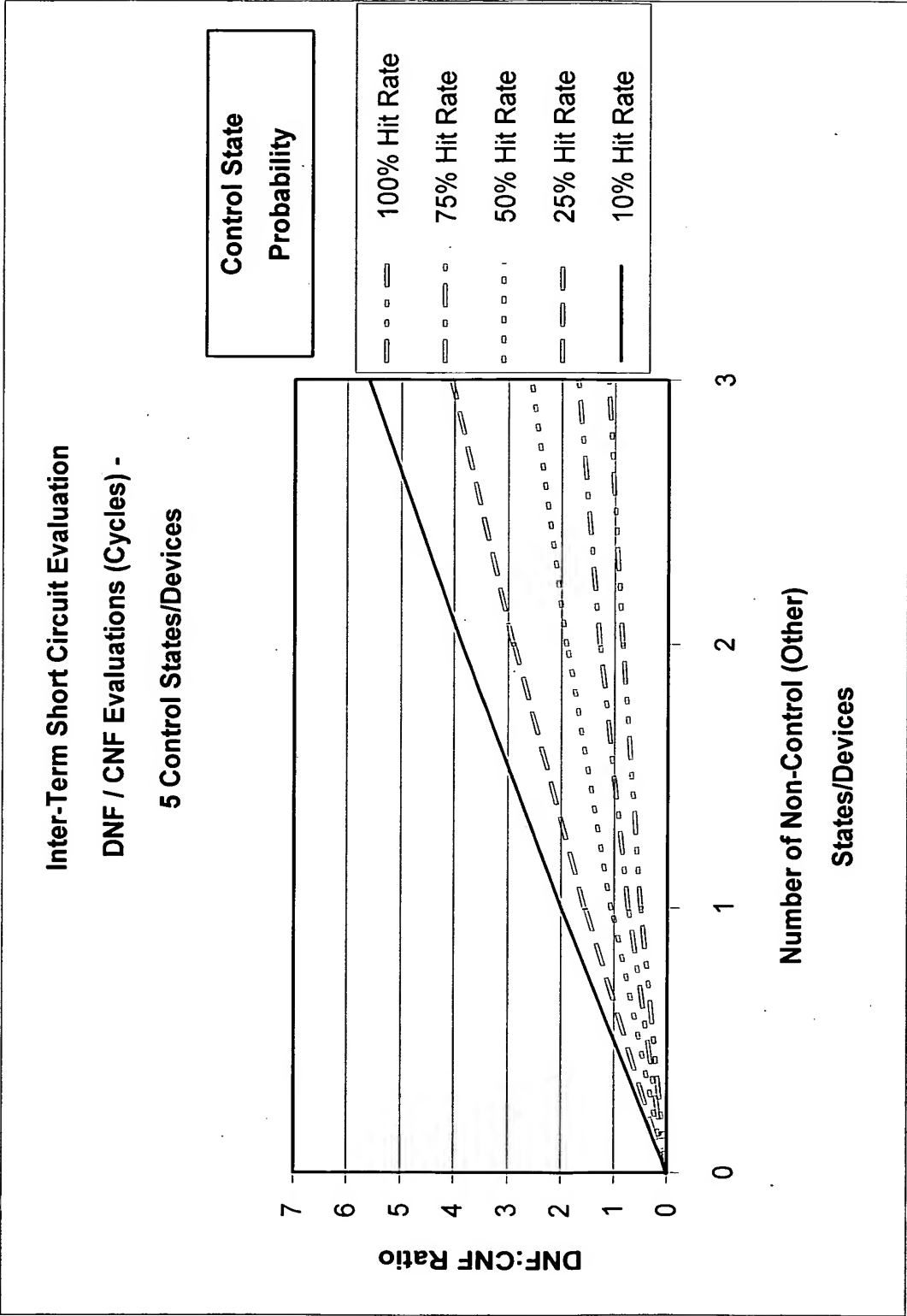


Fig. 8

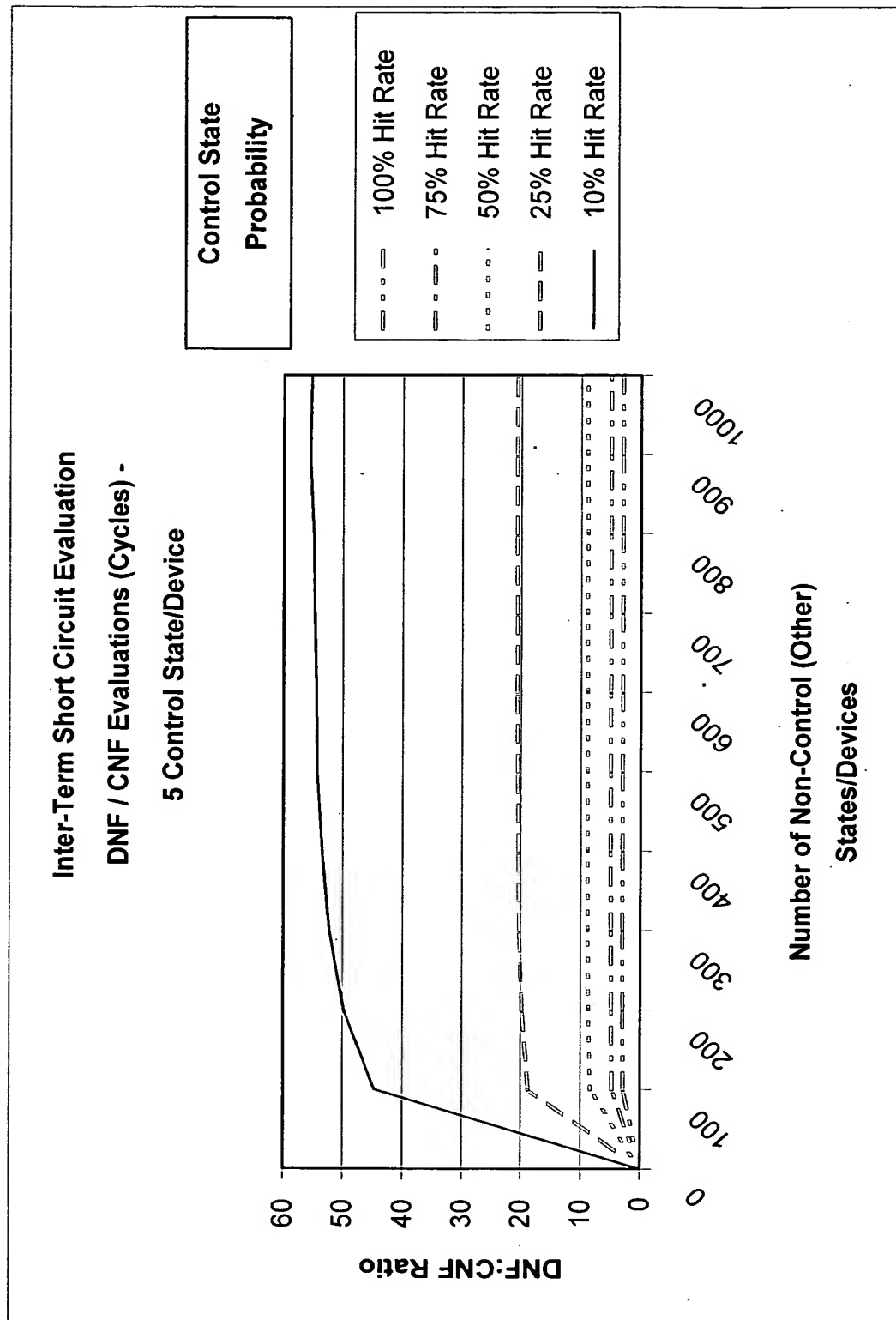


Fig. 9

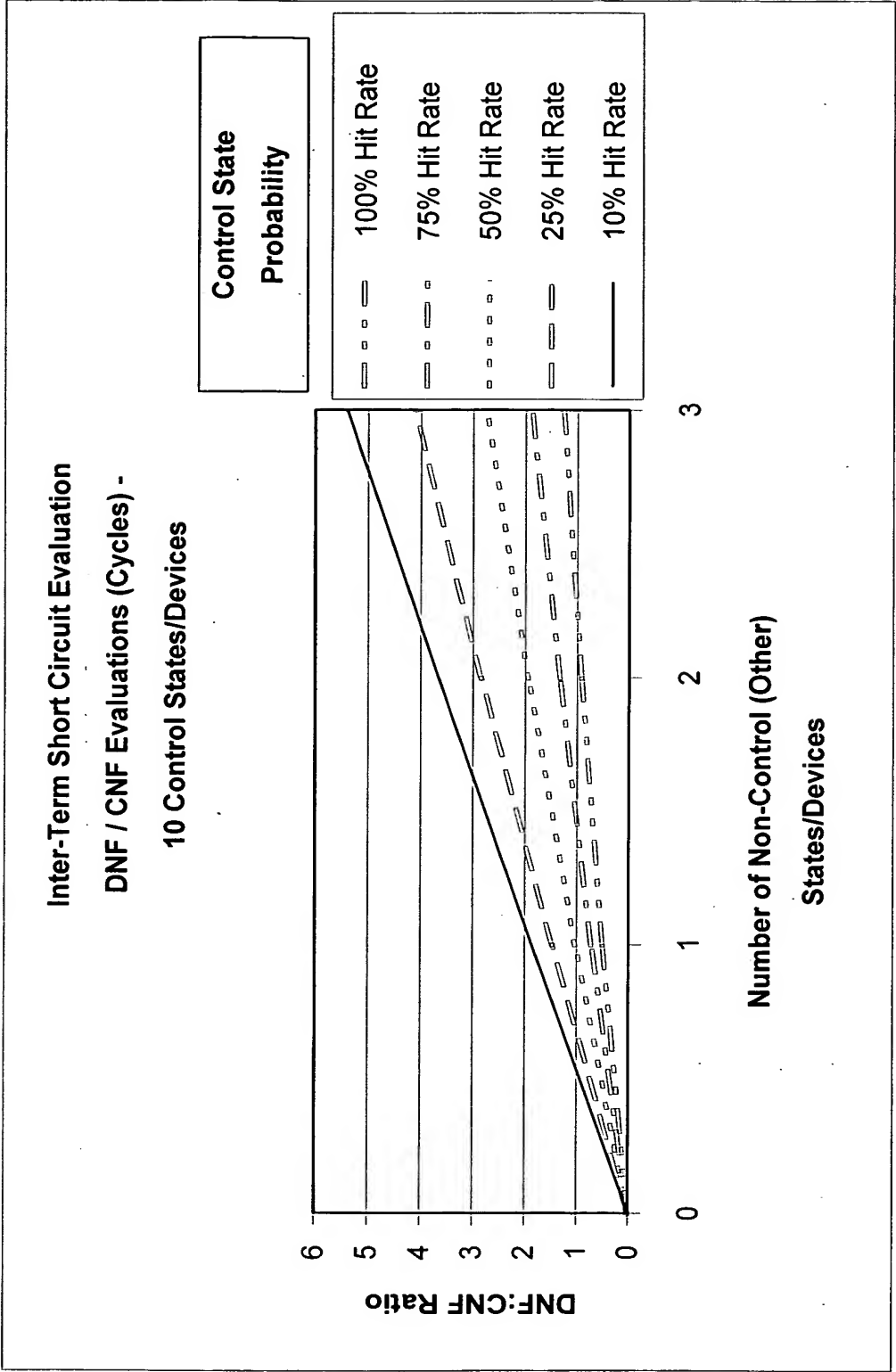


Fig. 10

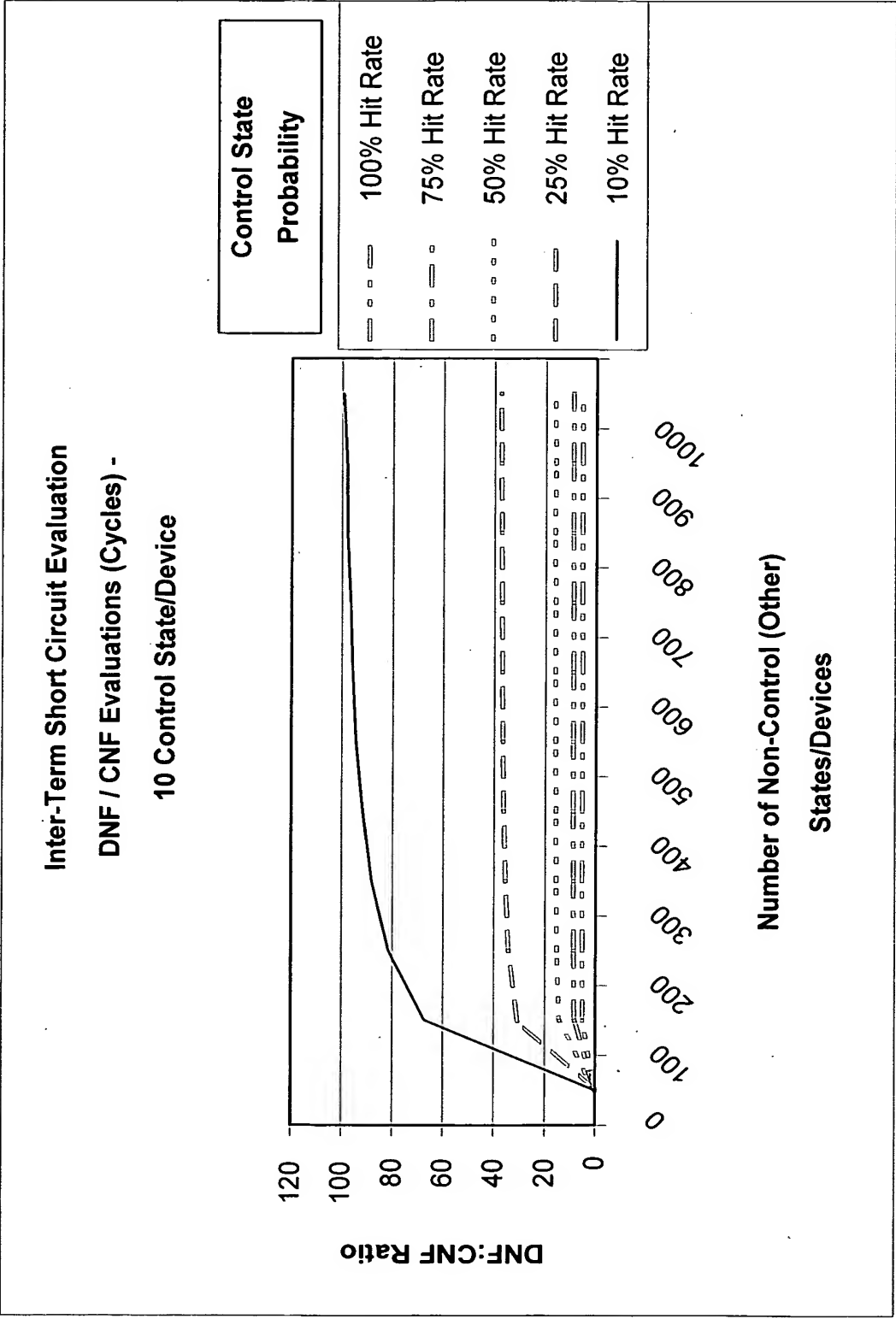


Fig. 11

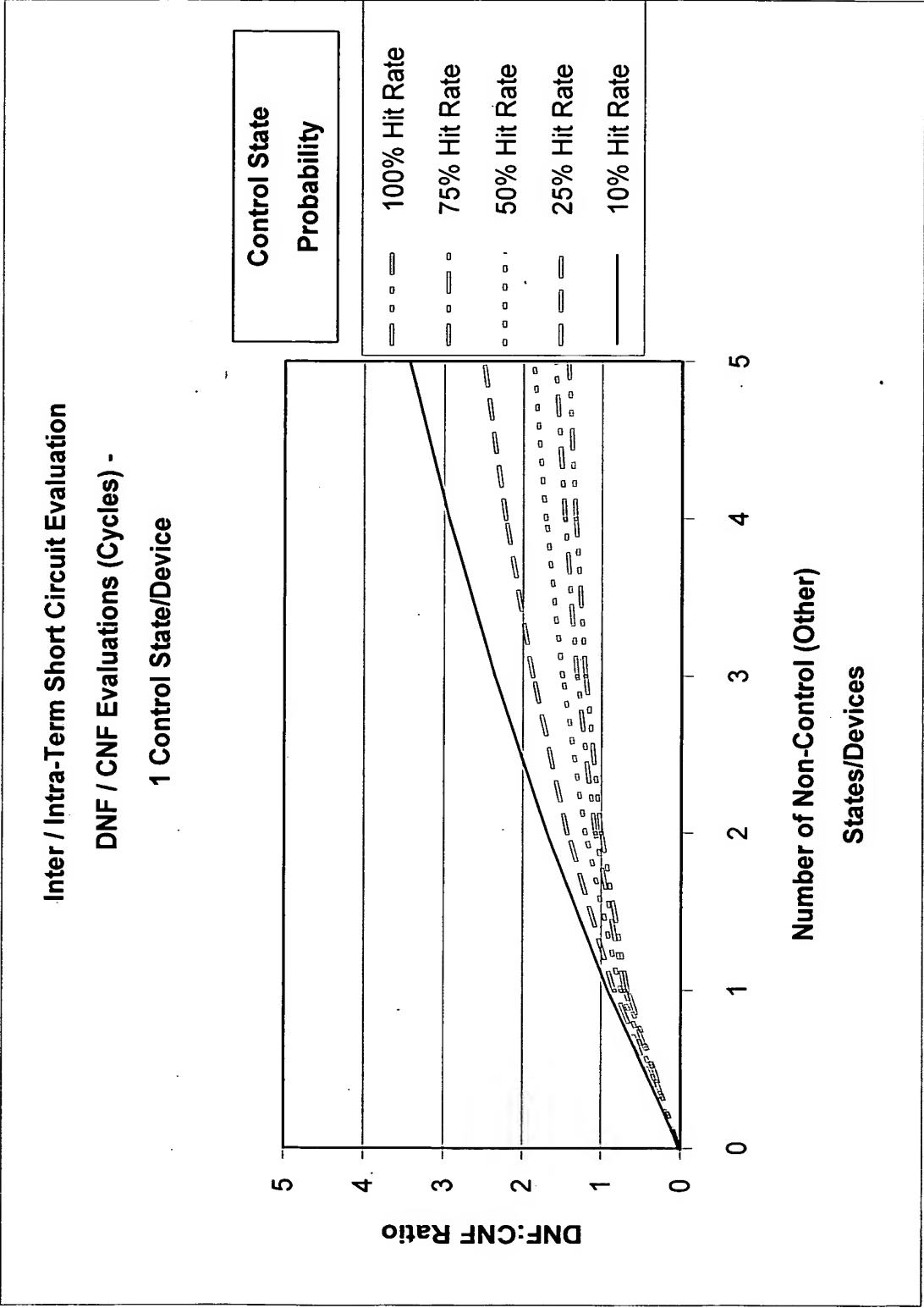


Fig. 12

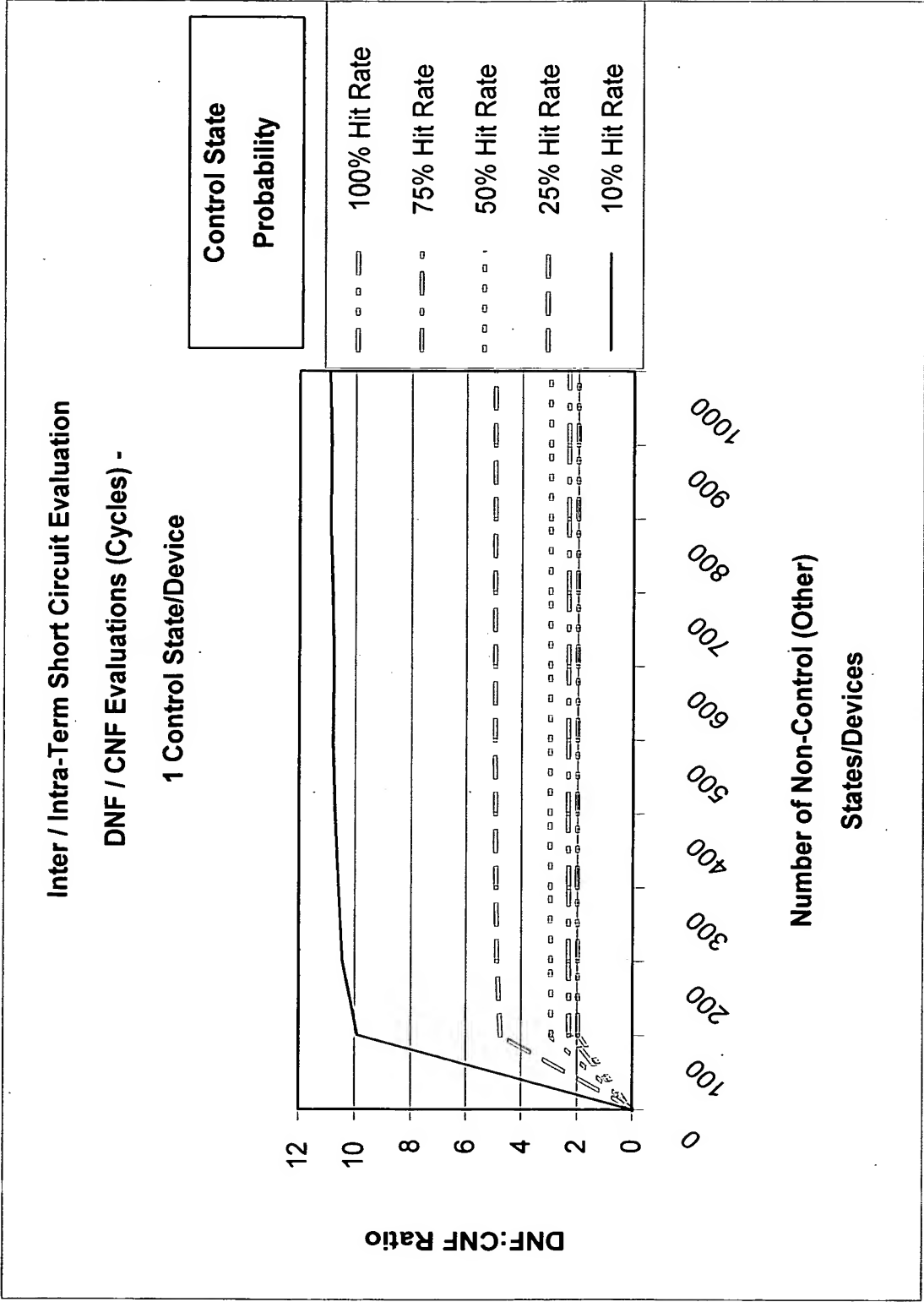


Fig. 13

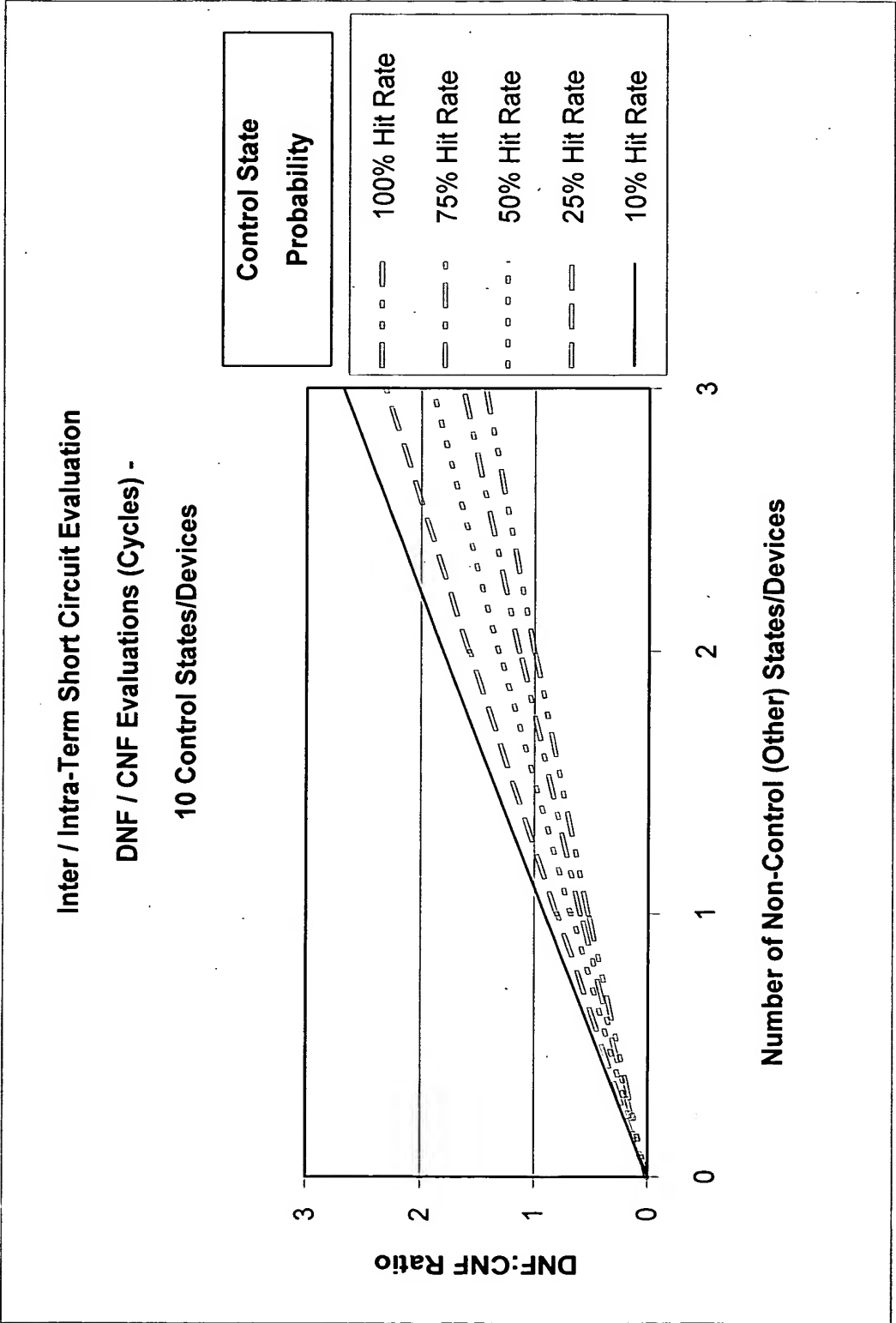


Fig. 14

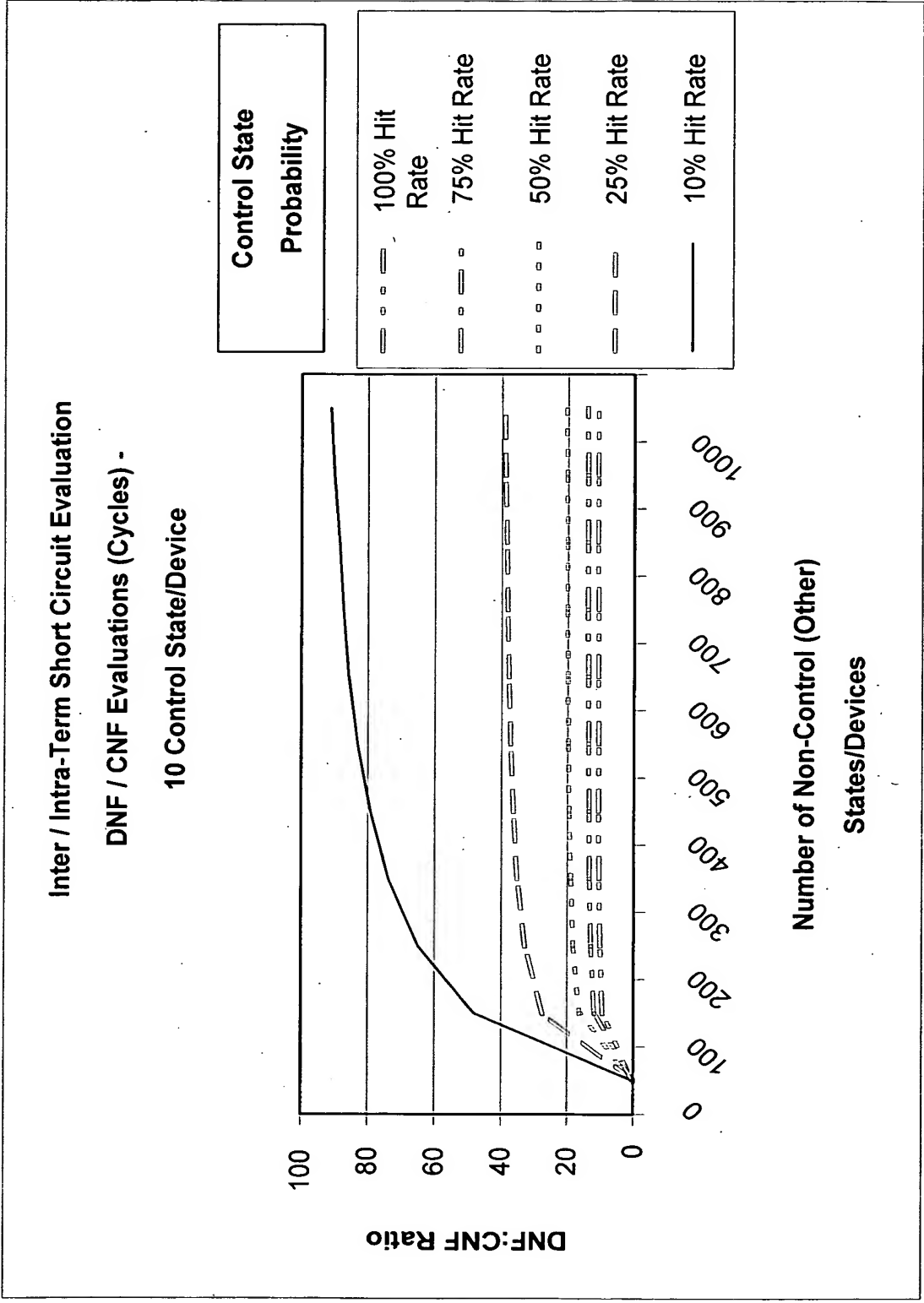


Fig. 15

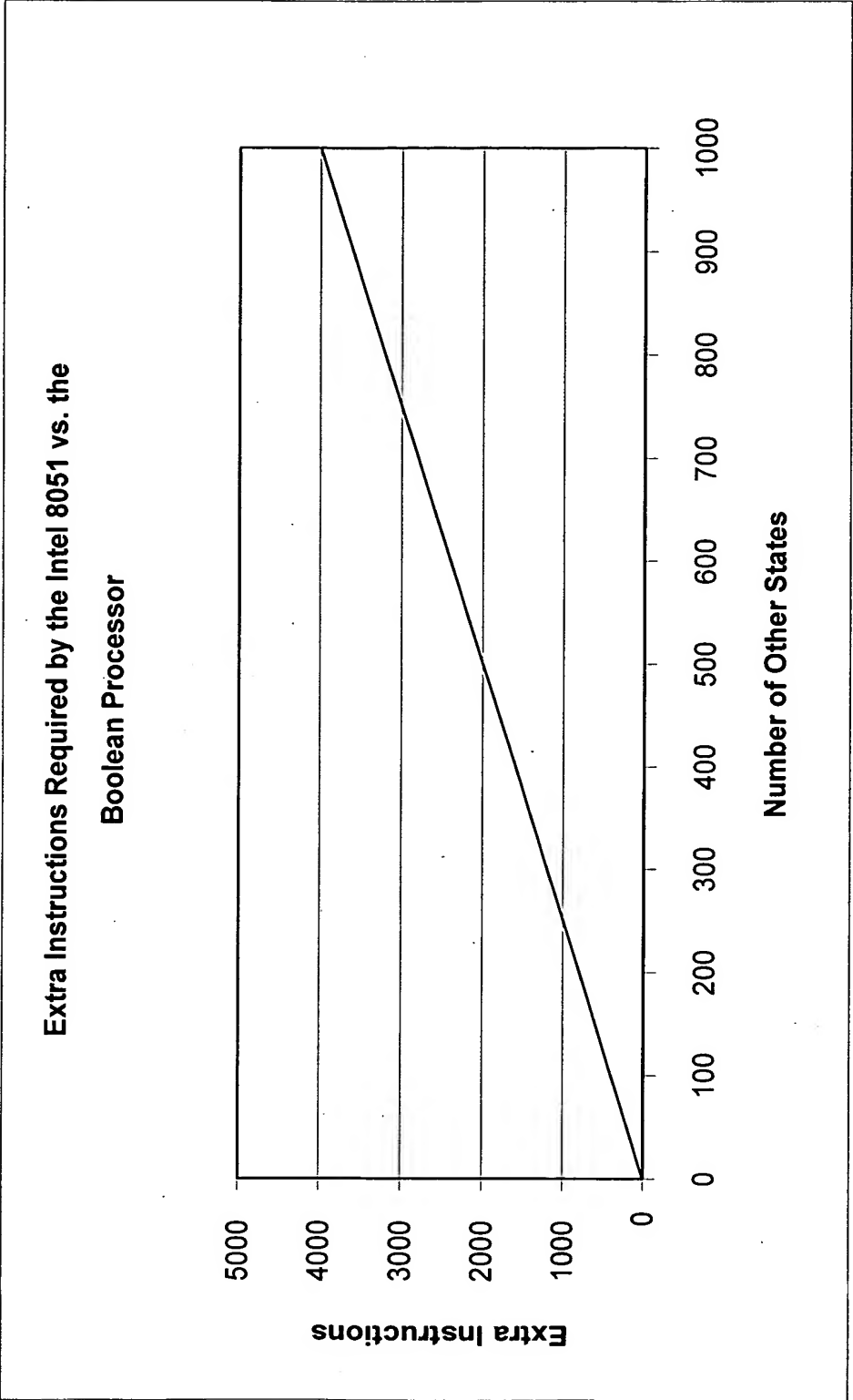


Fig. 16

**Extra Instructions Required by the
Intel 8086 Family of Microprocessors
vs. the Boolean Processor**

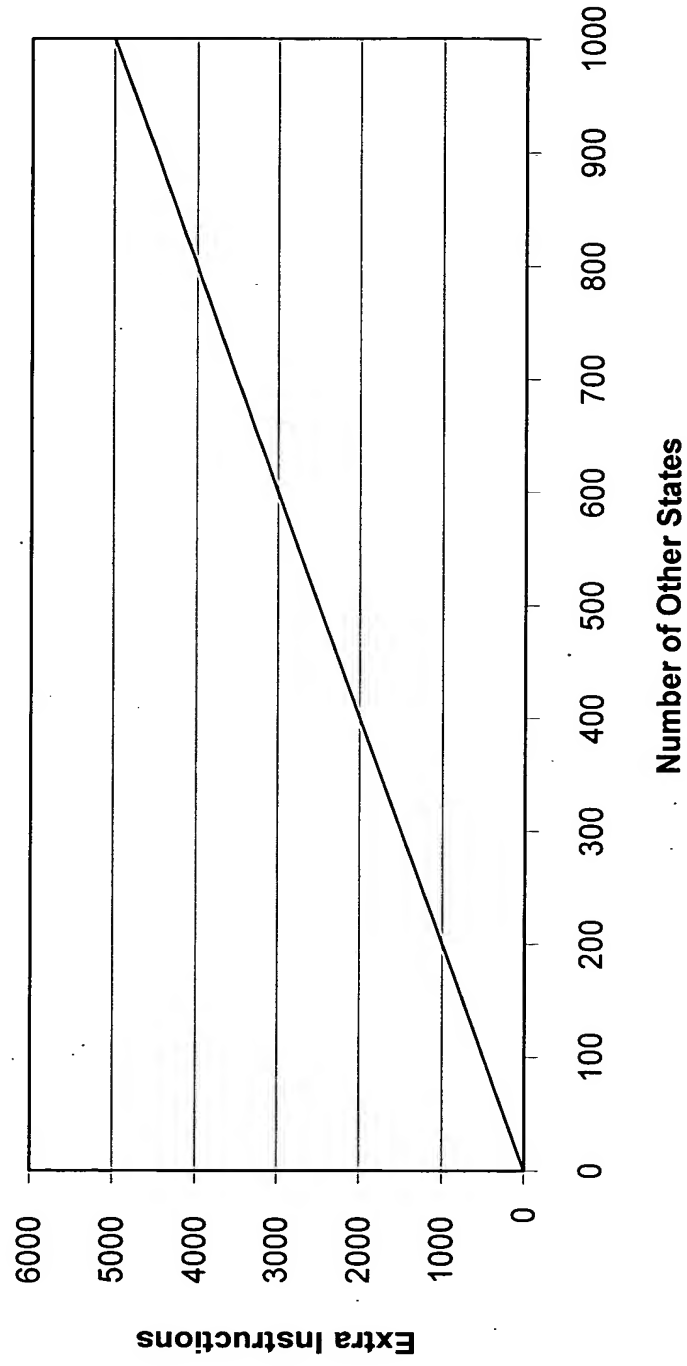


Fig. 17

**Extra Instructions Required by the
Motorola MMC2107 Microcontroller
vs. the Boolean Processor**

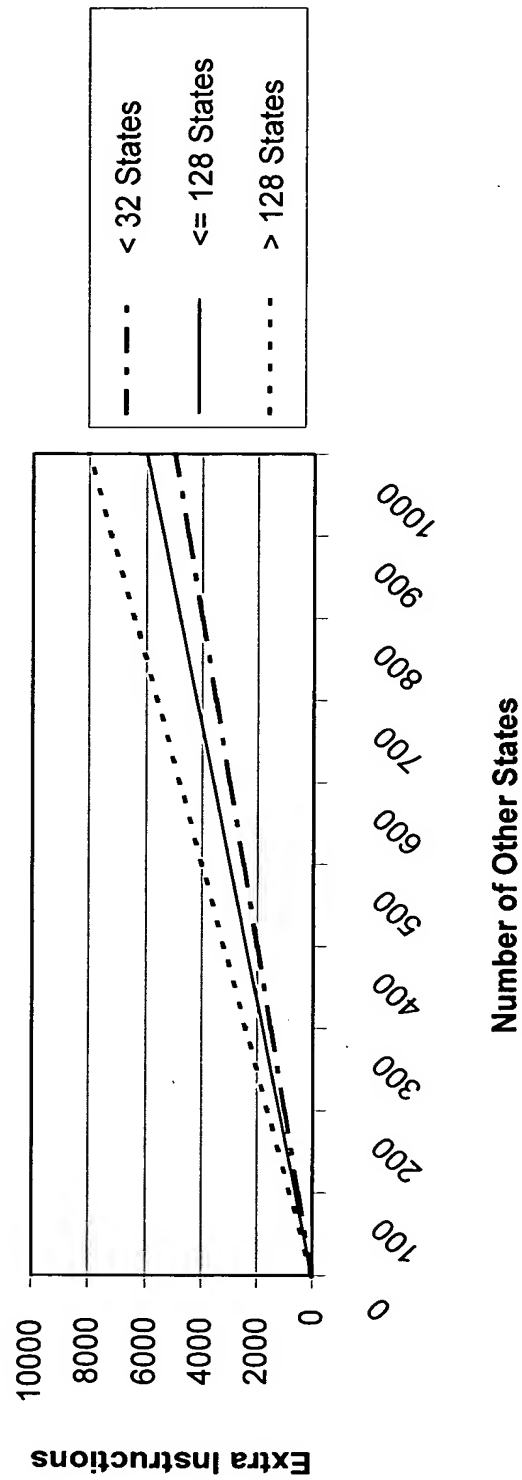


Fig. 18

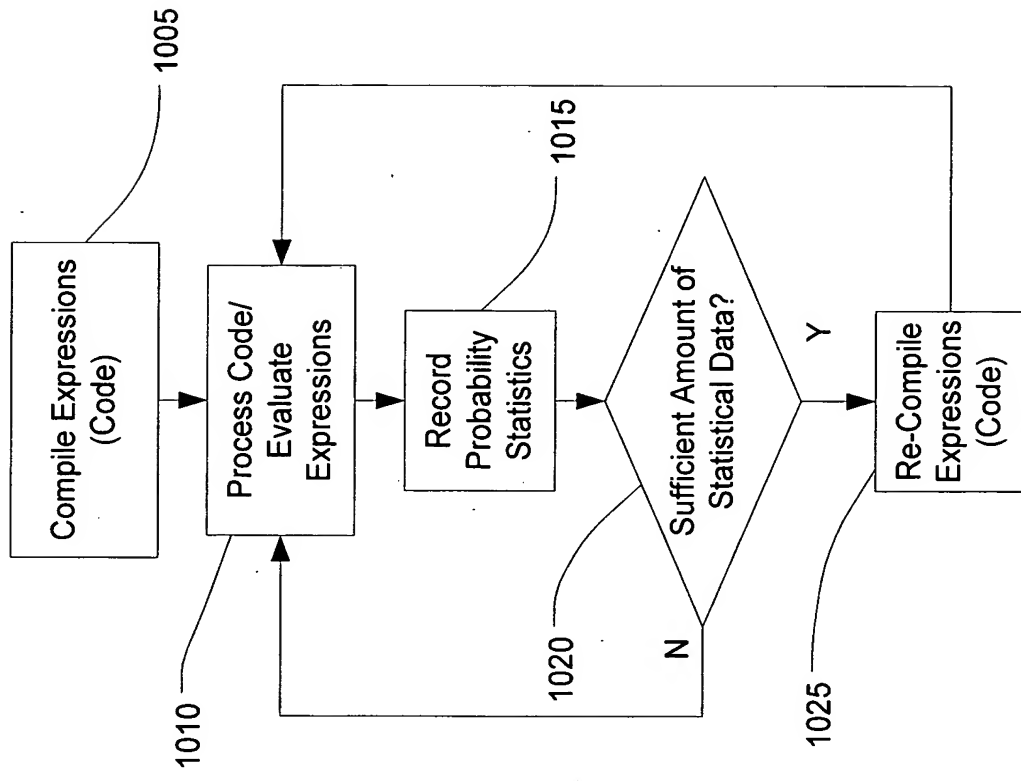


Fig. 19

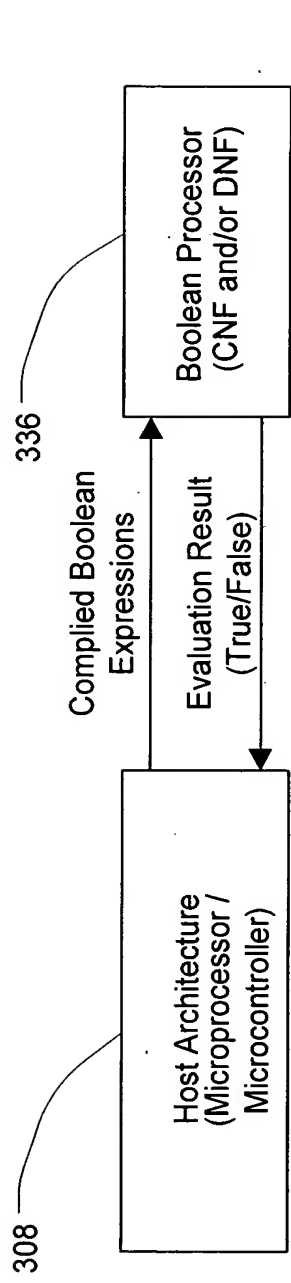


Fig. 20

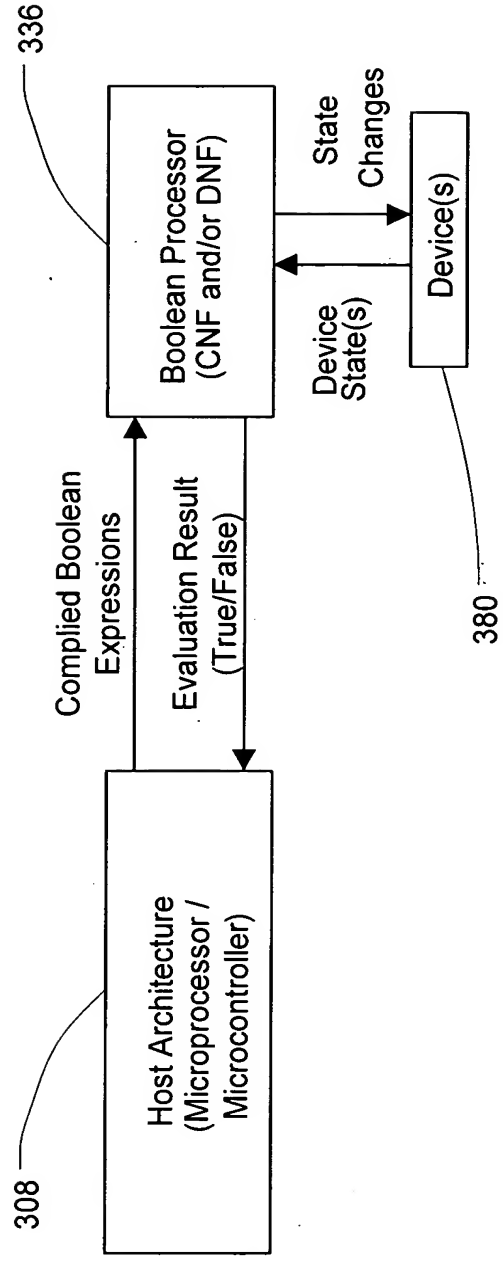


Fig. 21

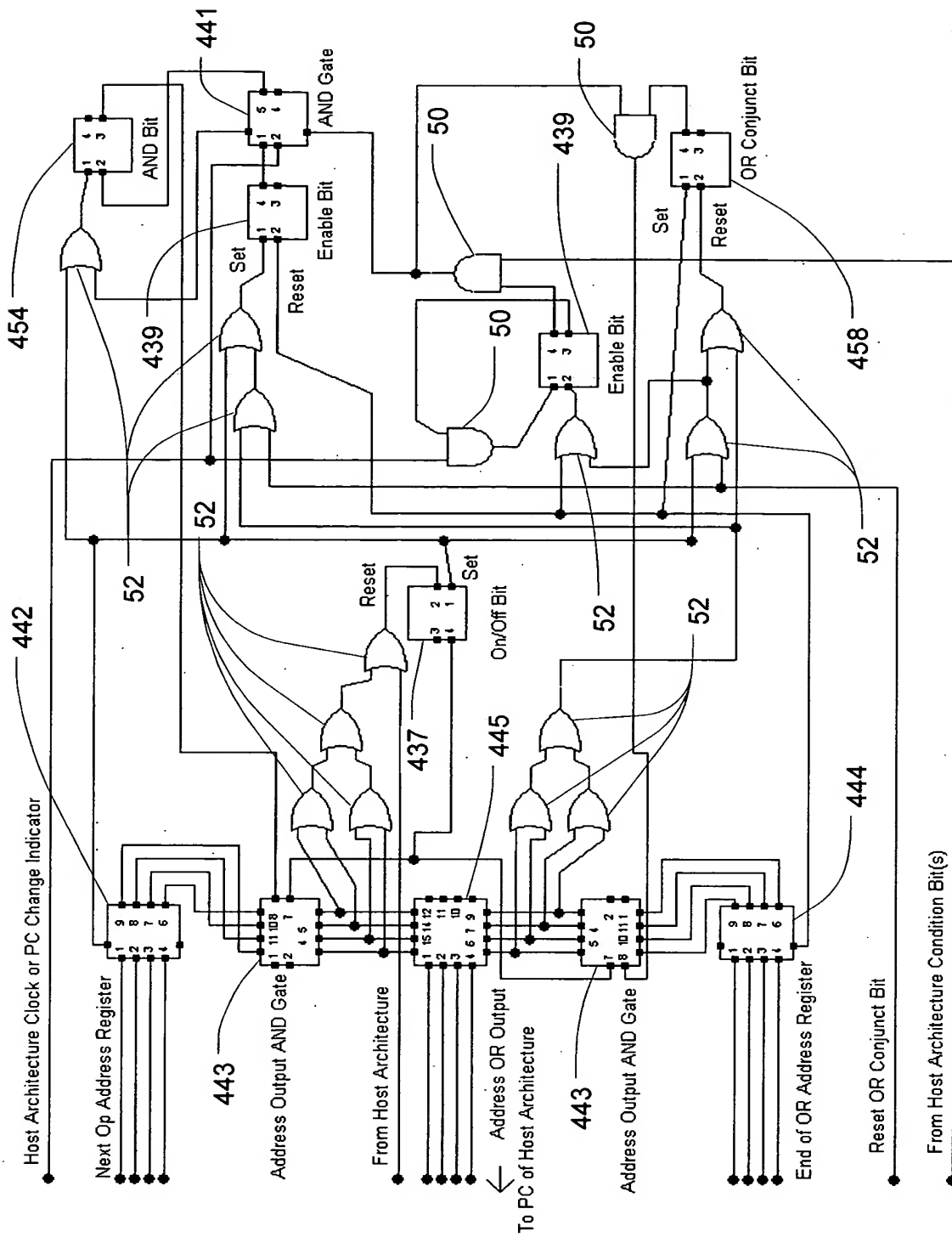


Fig. 22

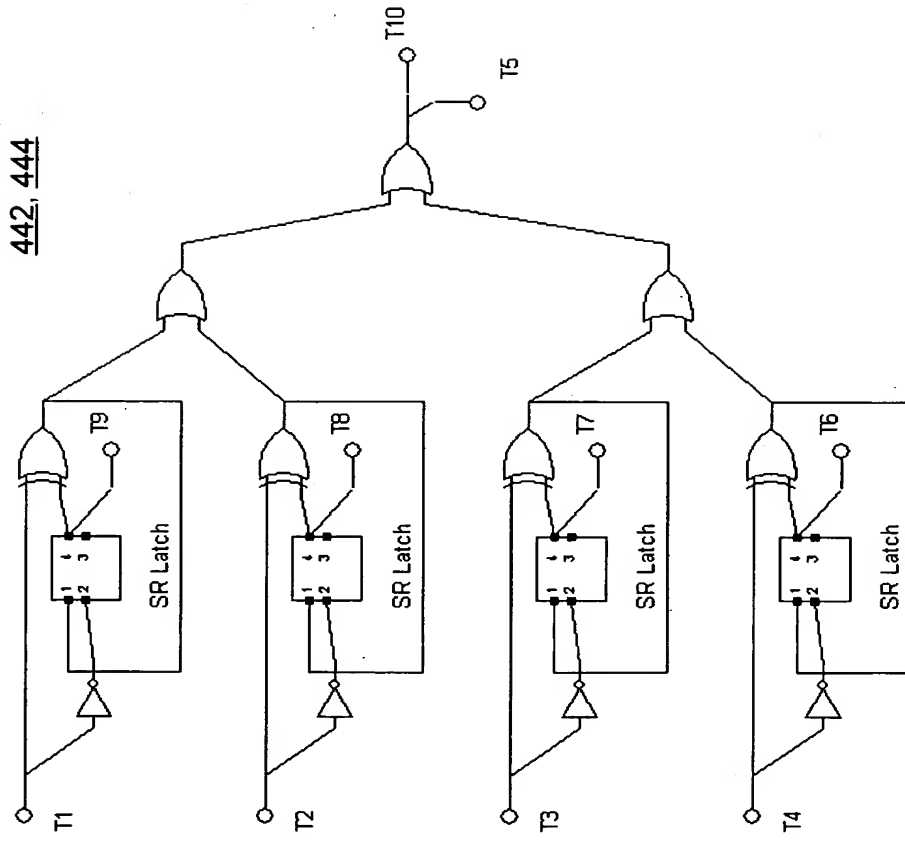


Fig. 23

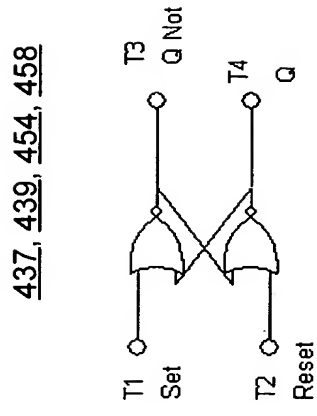


Fig. 24

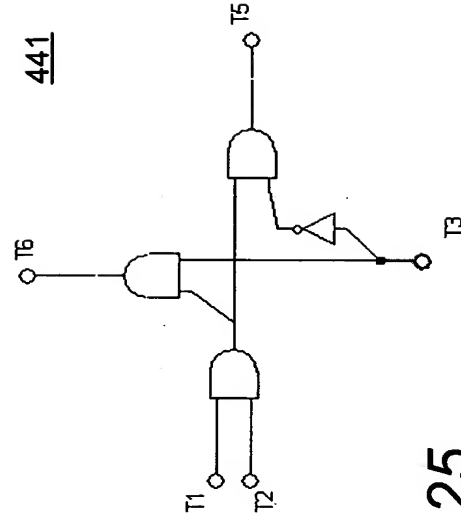


Fig. 25

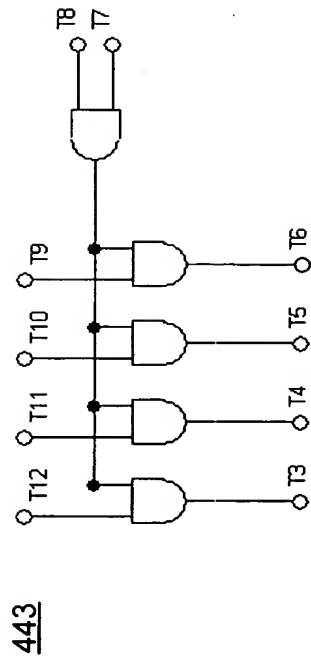


Fig. 26

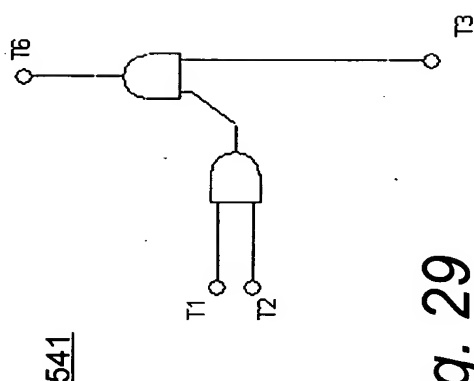


Fig. 29

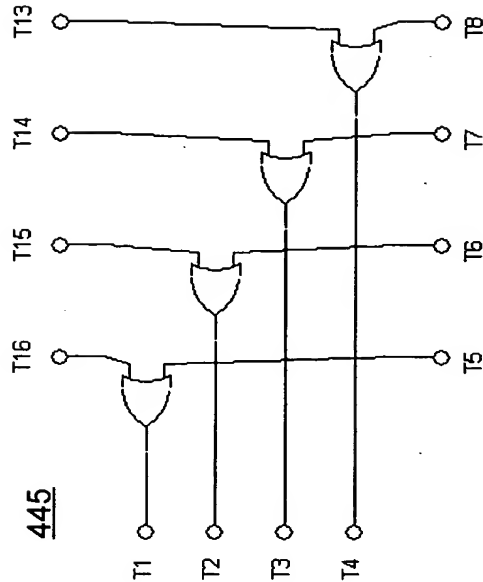


Fig. 27

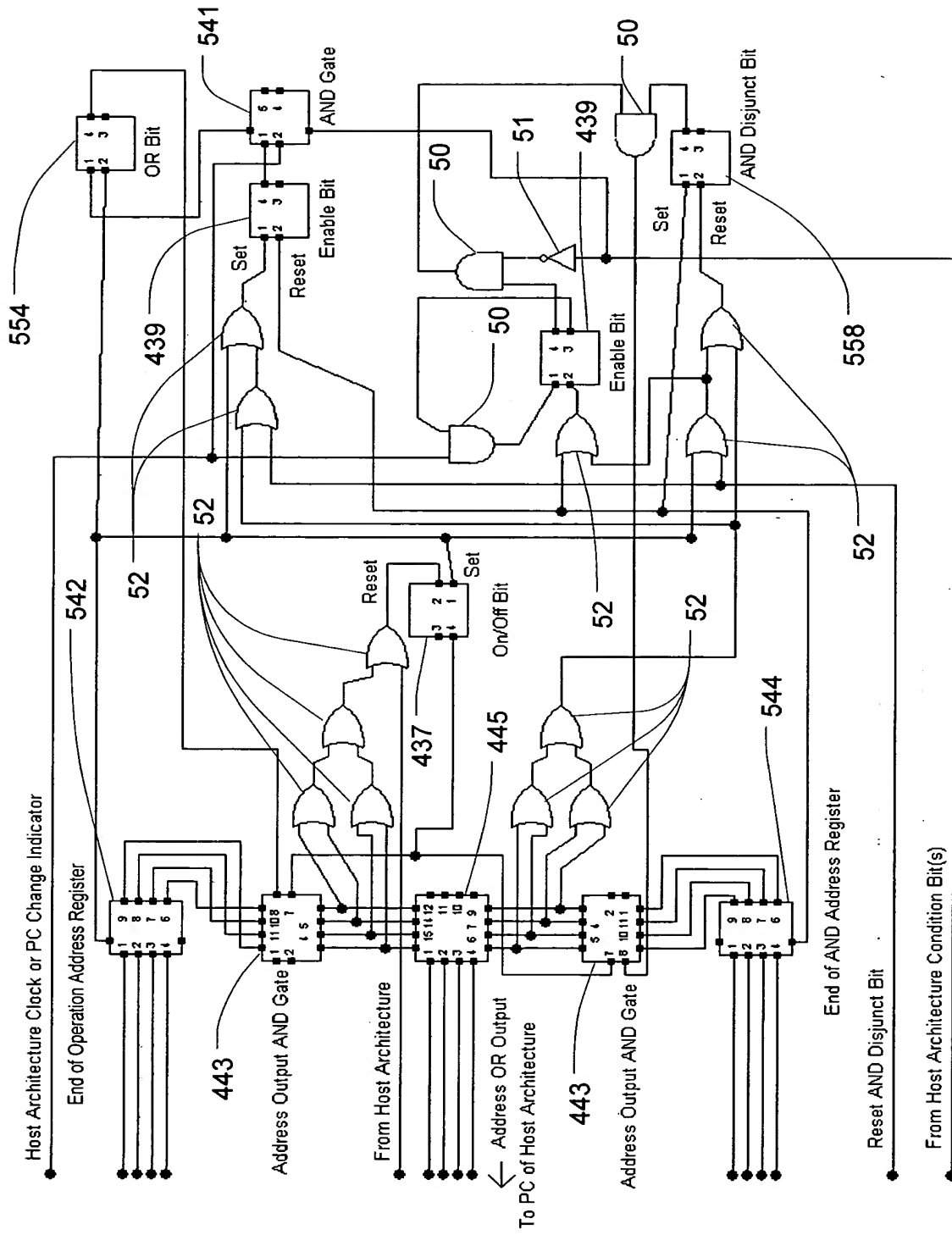


Fig. 28

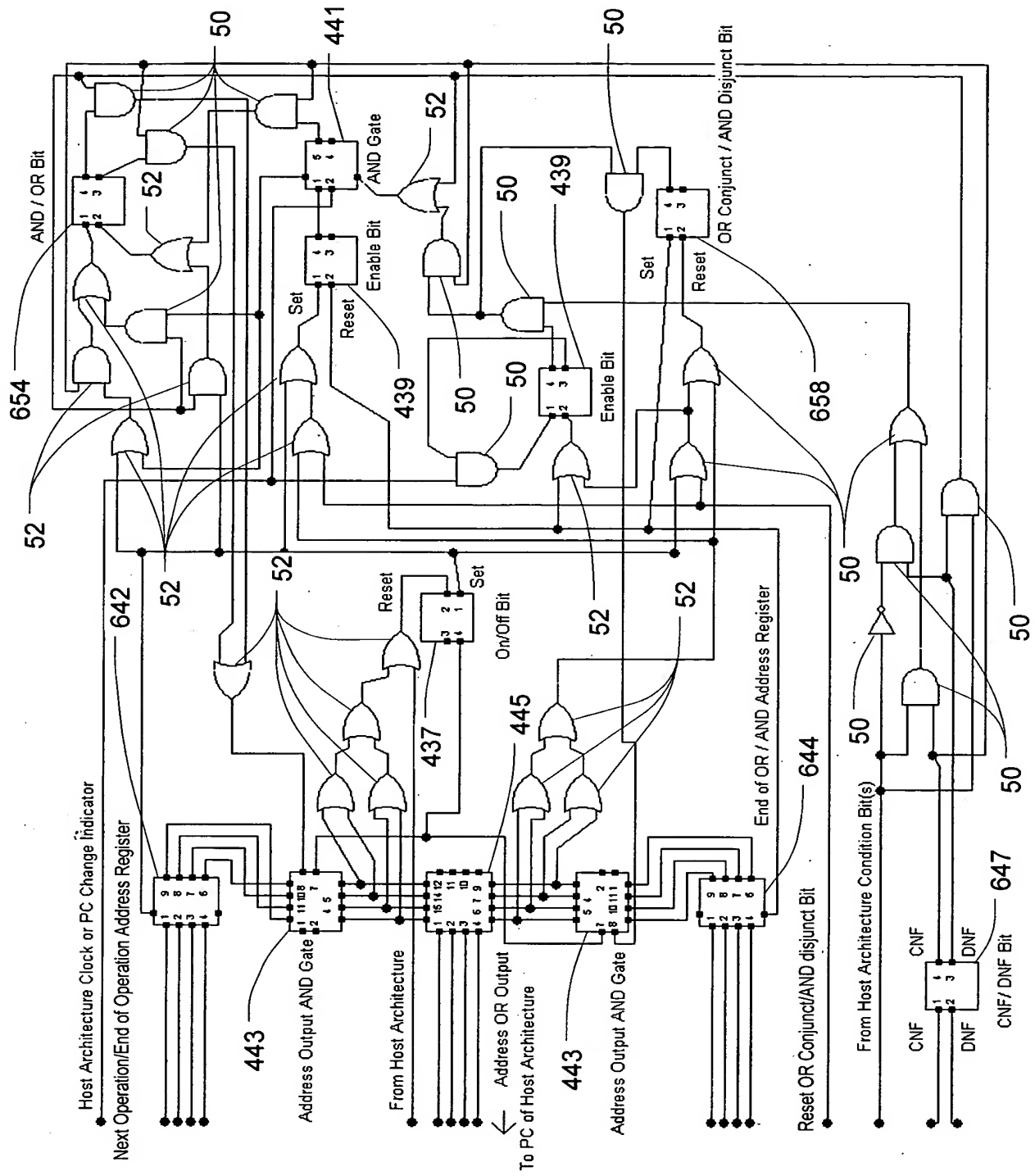


Fig. 30

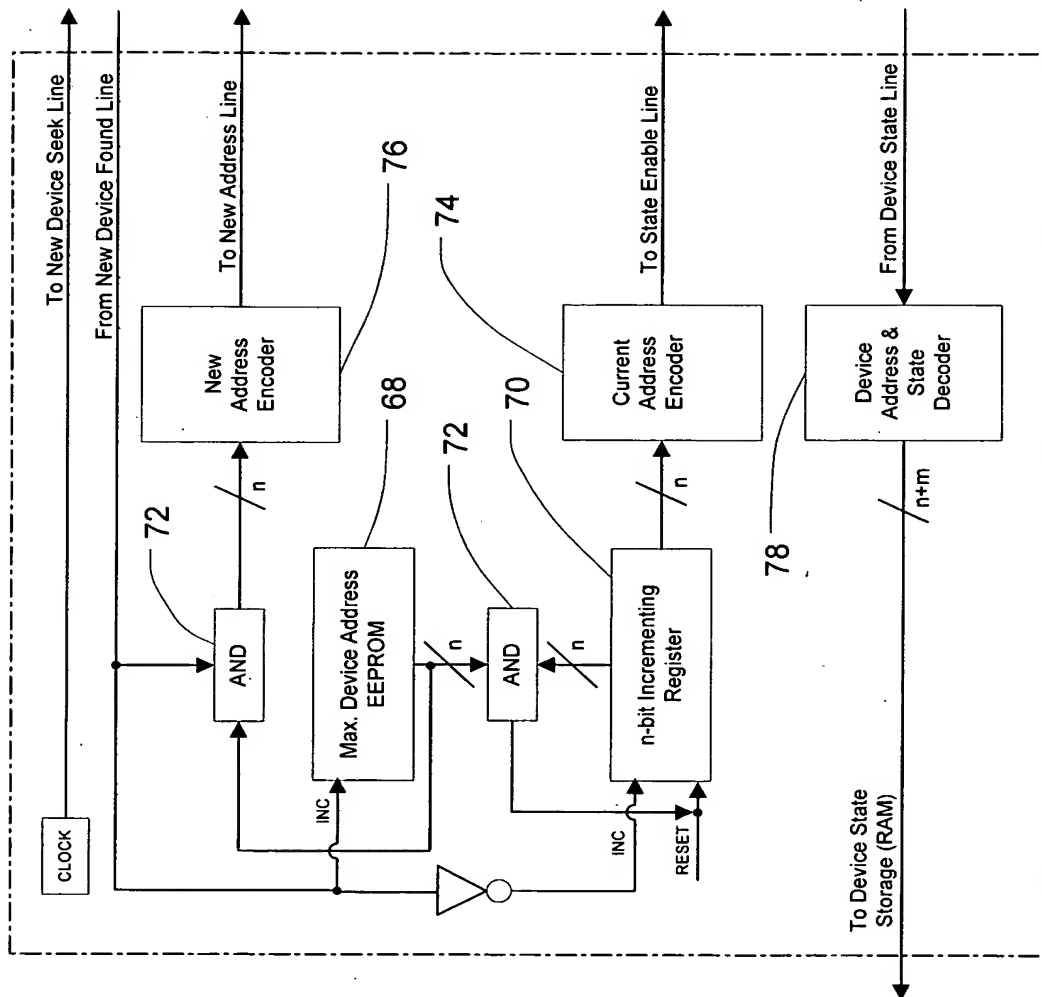


Fig. 31

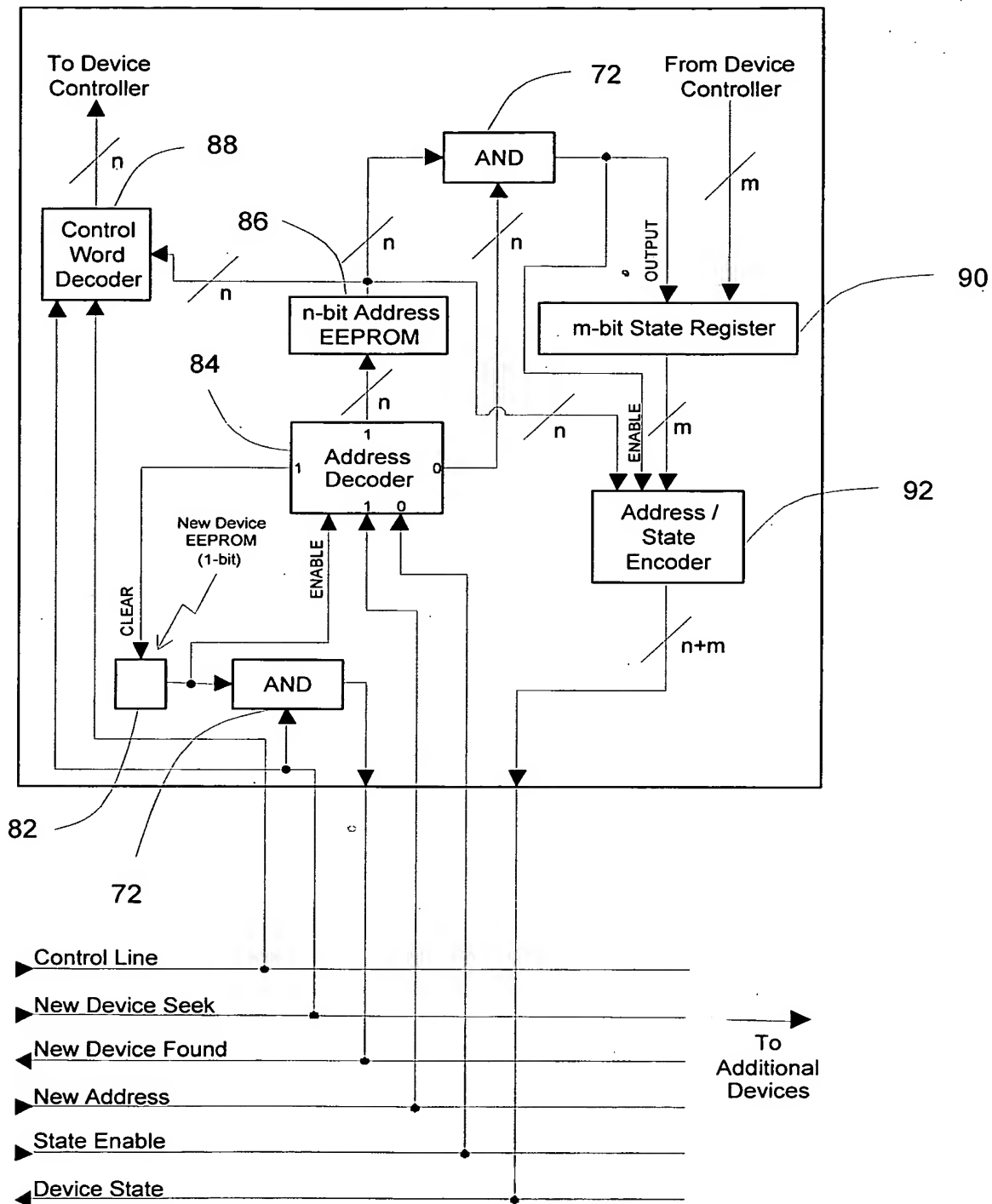


Fig. 32

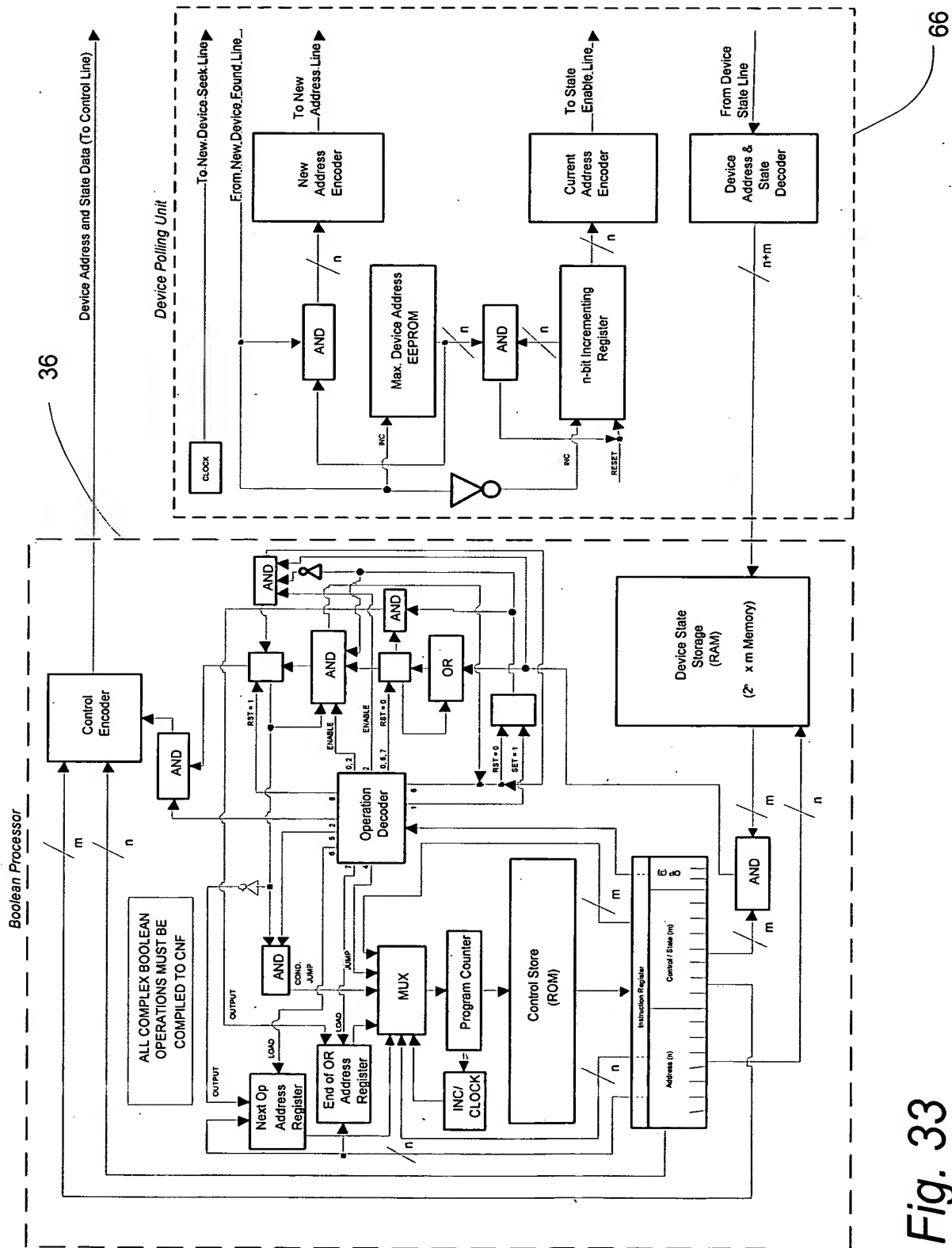
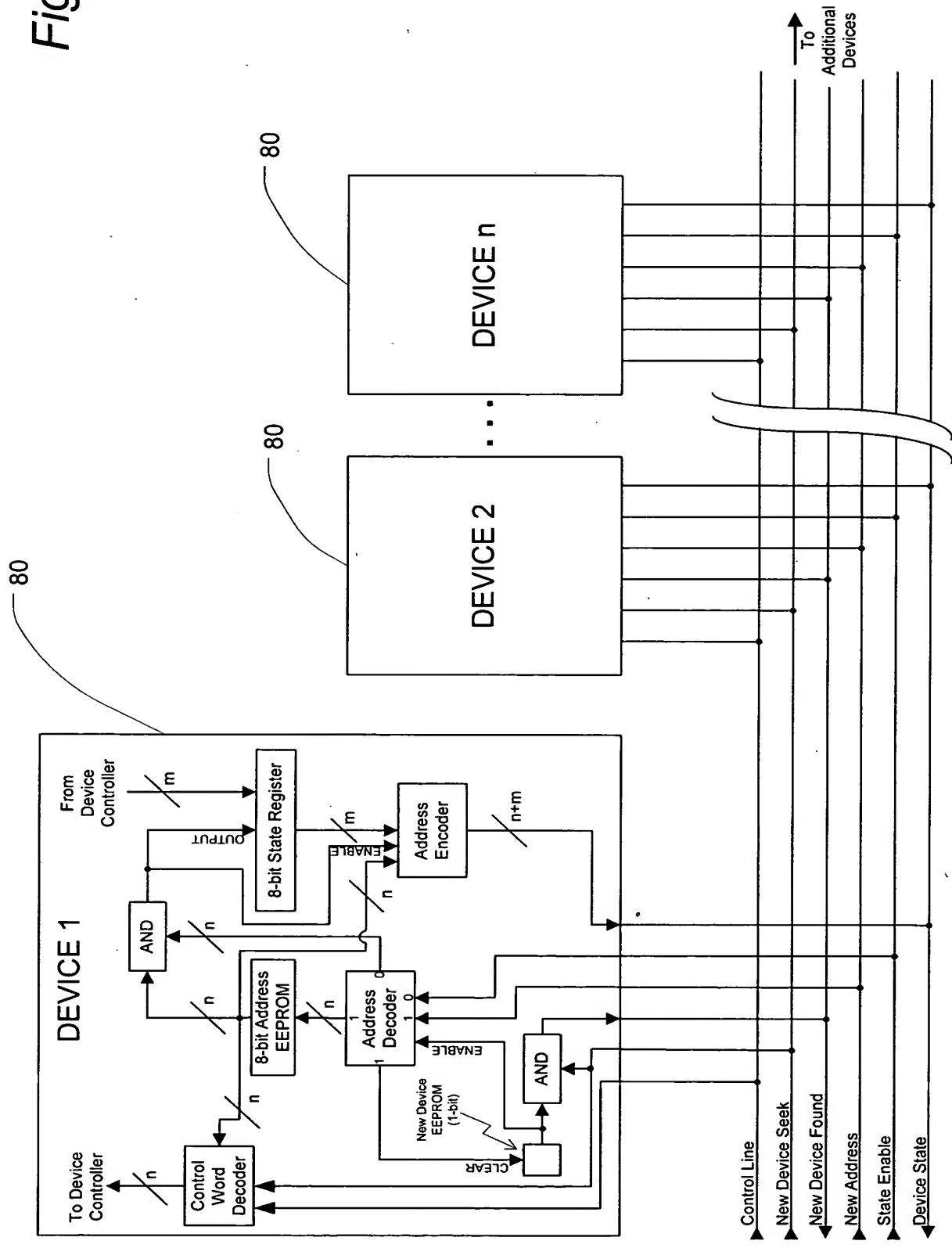


Fig. 33

Fig. 34



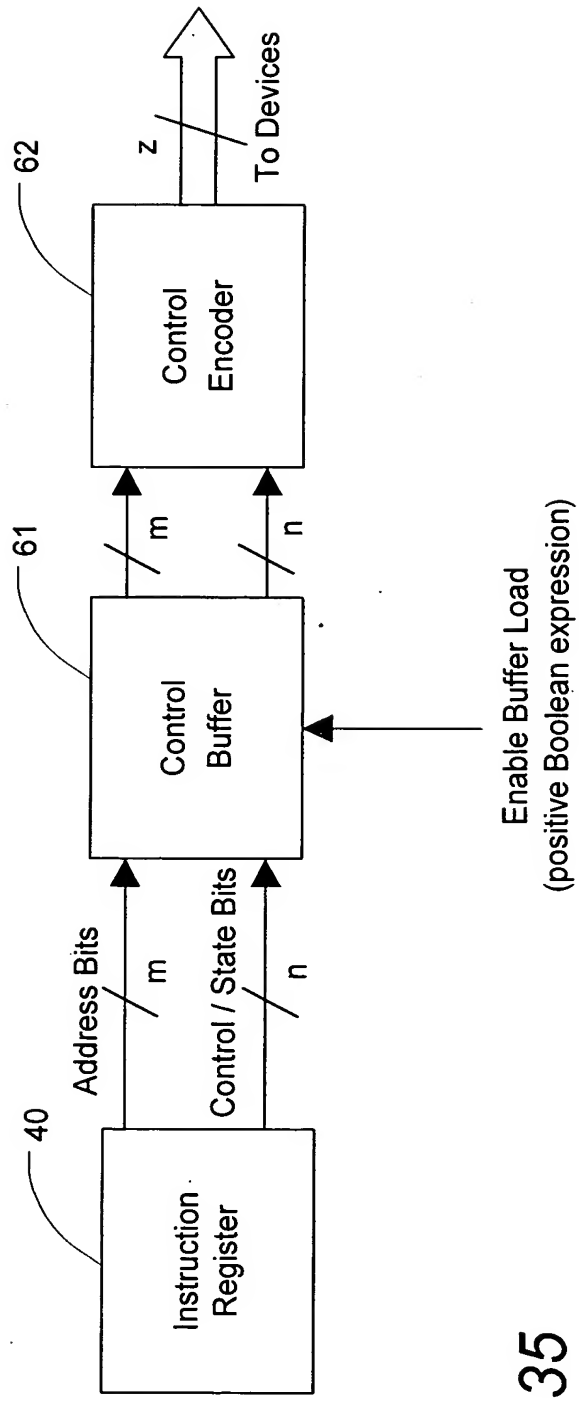


Fig. 35

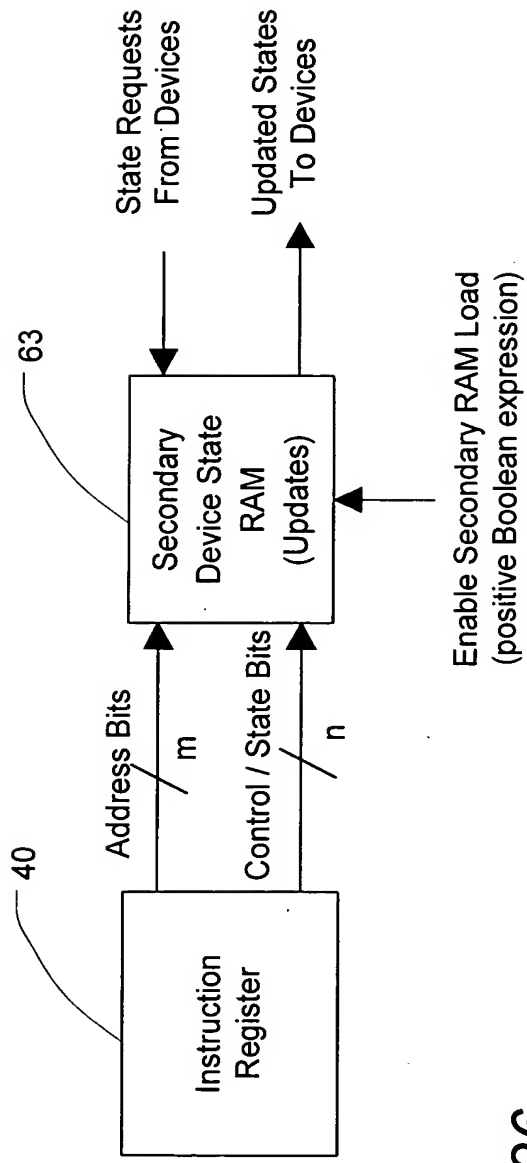


Fig. 36

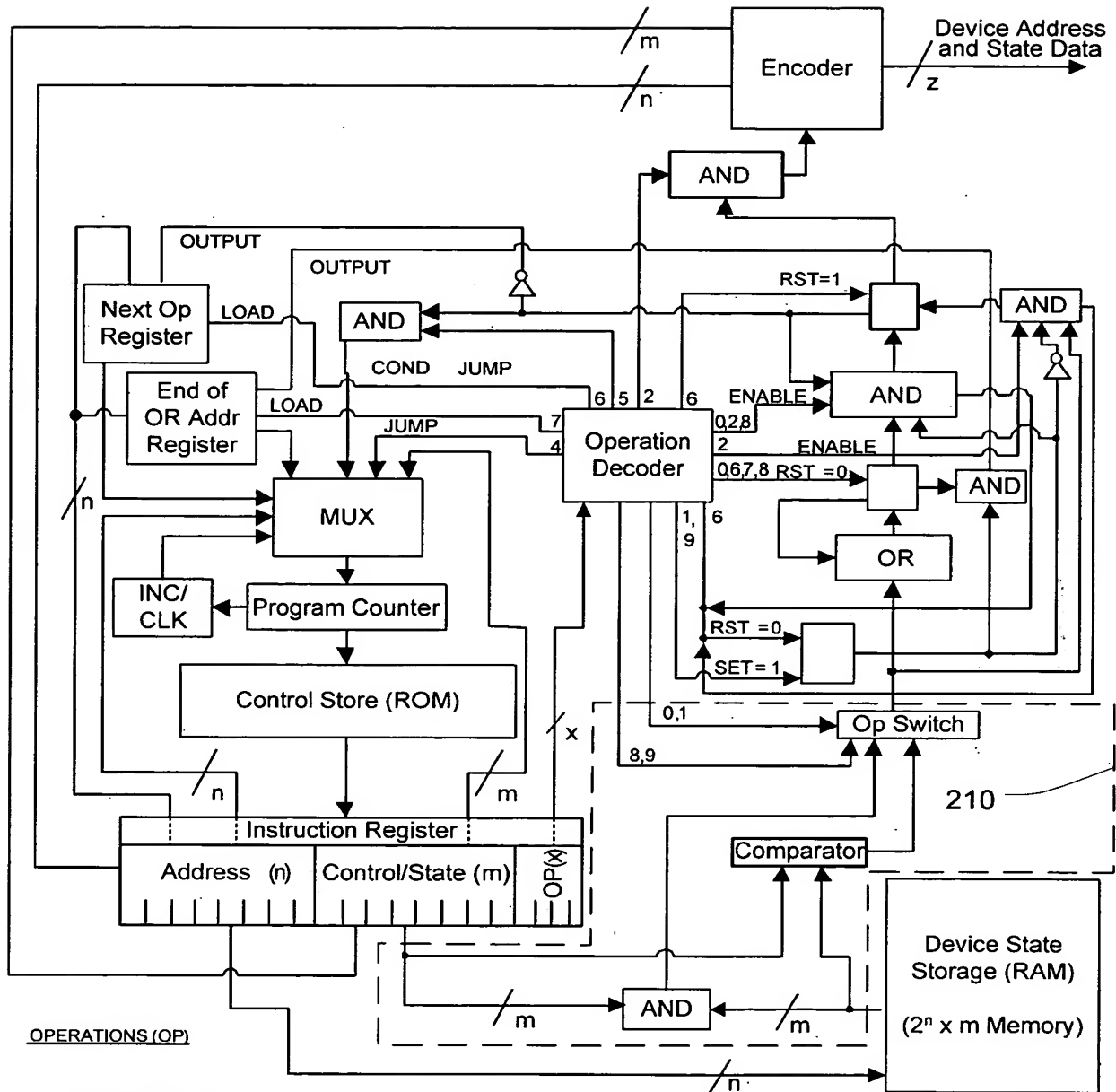


Fig. 37

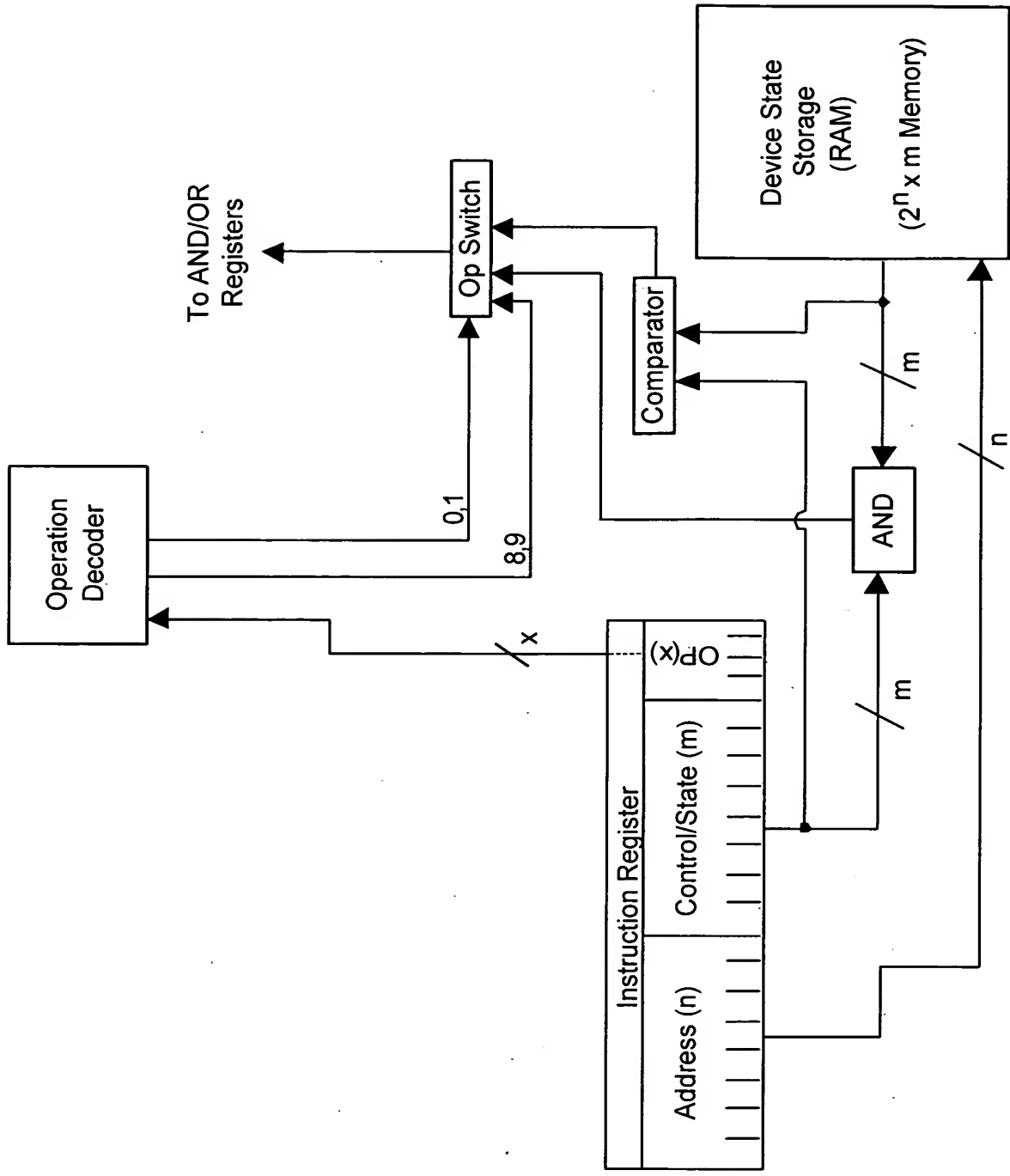


Fig. 38

Fig. 39

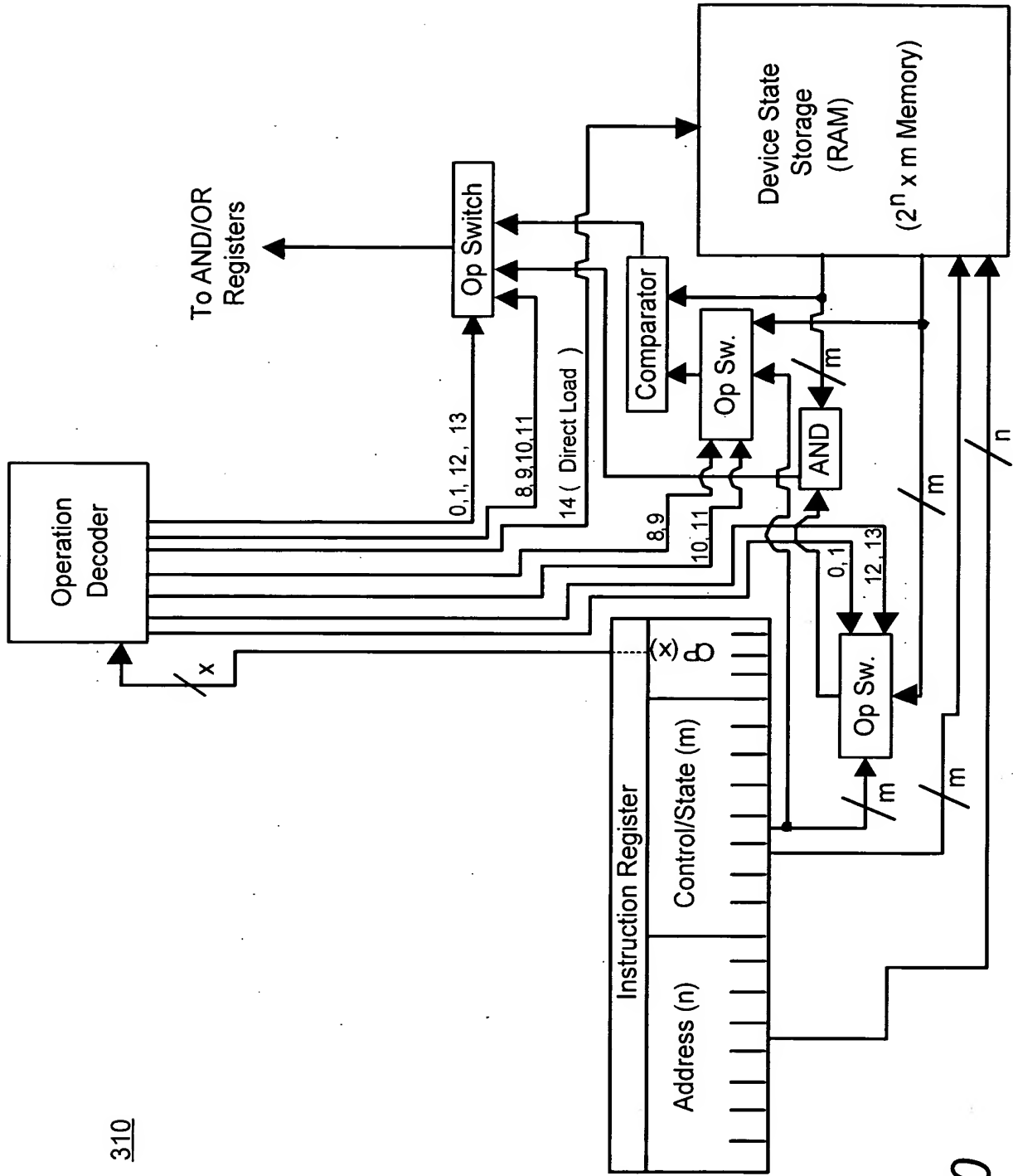


Fig. 40

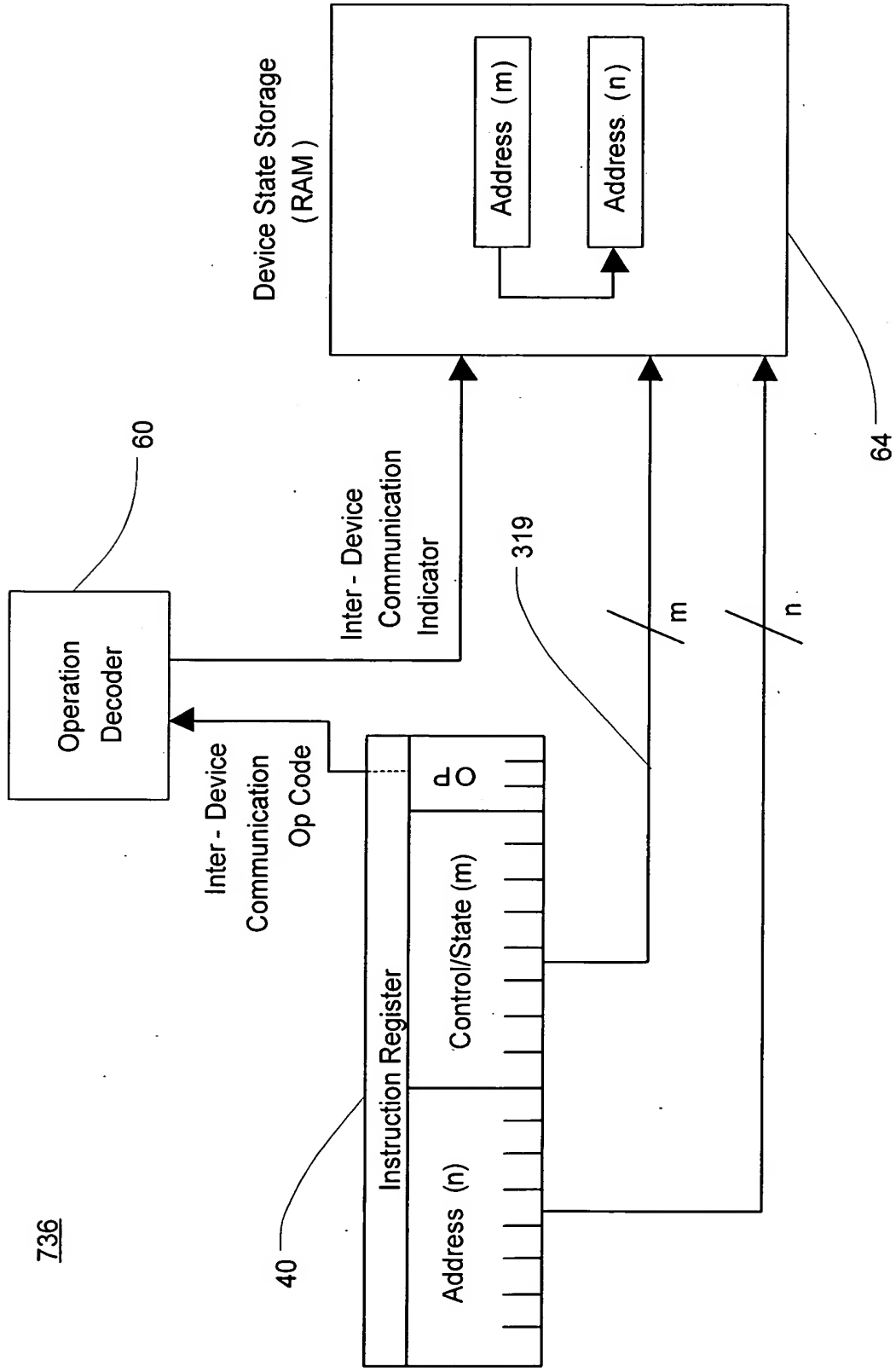


Fig. 41

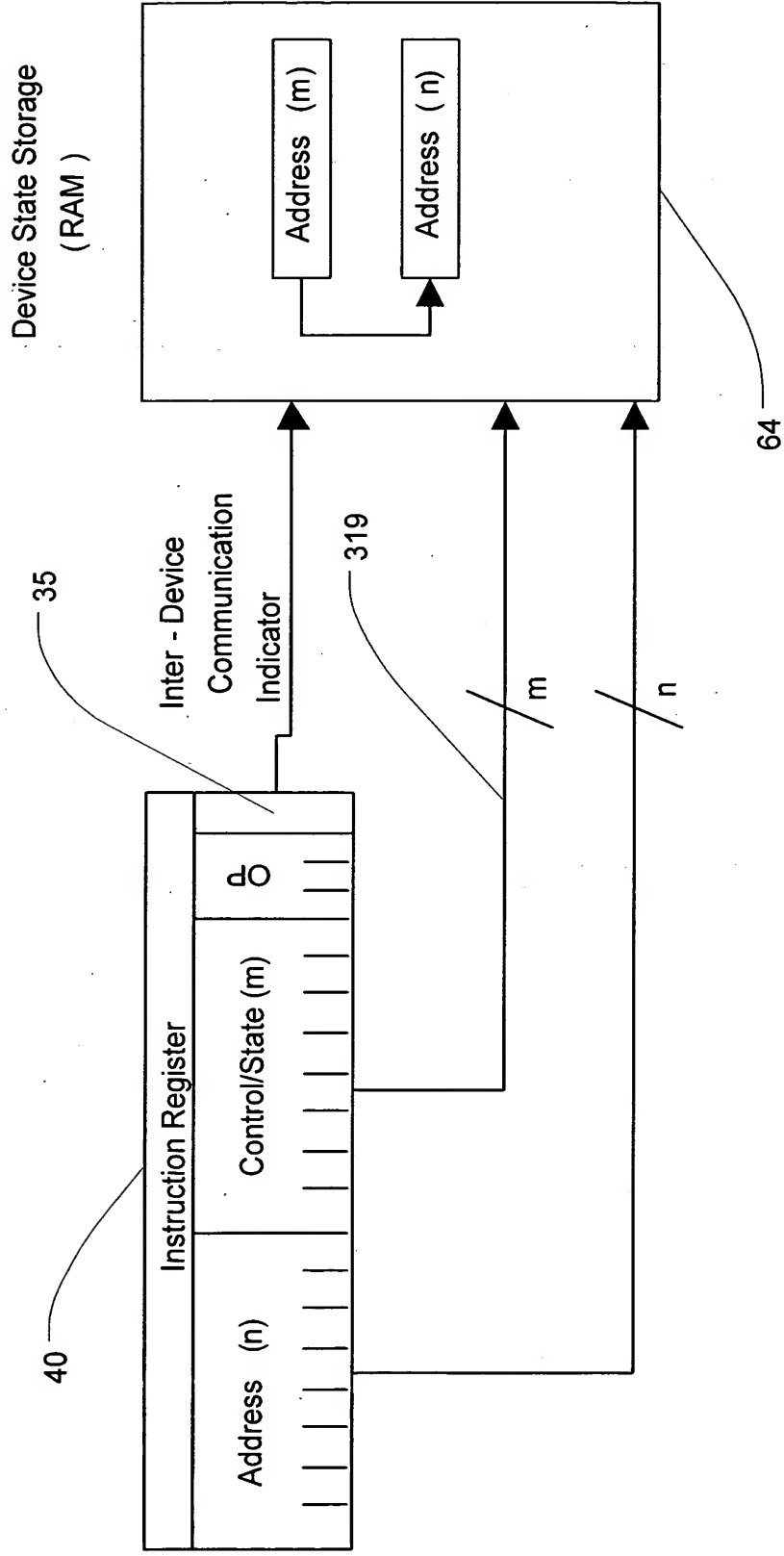


Fig. 42

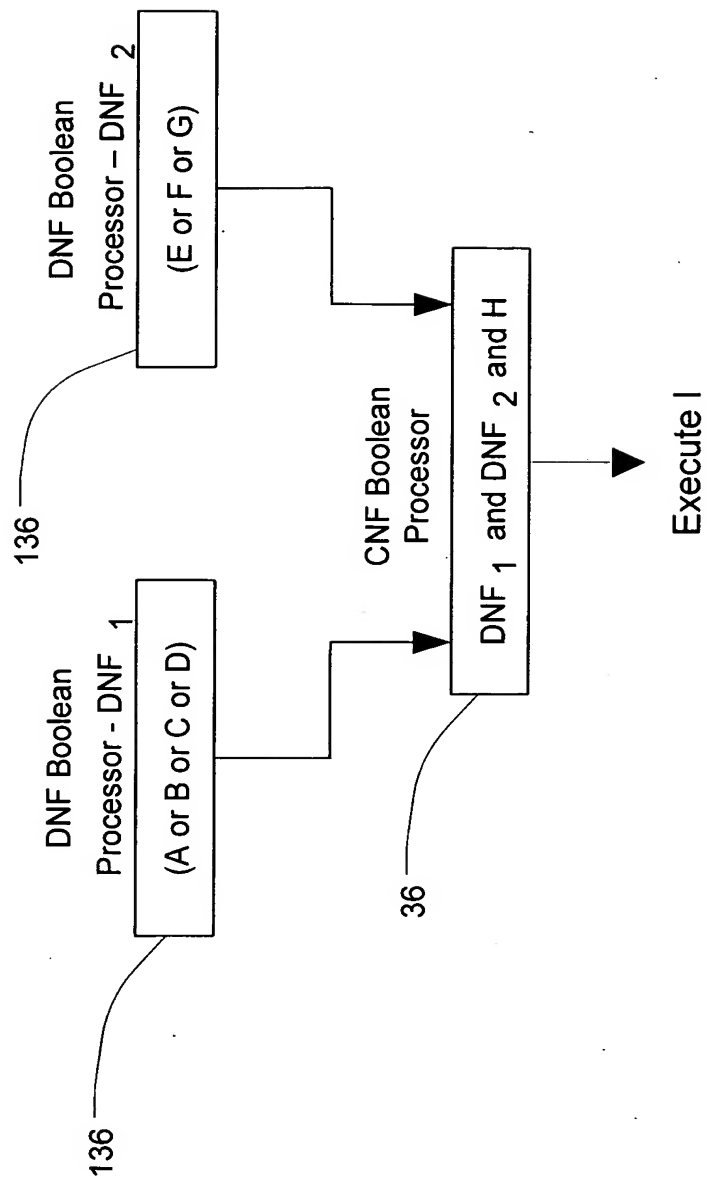


Fig. 43